## Novel architectures for braininspired photonic computers

Nieuwe architecturen voor breingeïnspireerde fotonische computers

#### Floris Laporte

#### 2020.03.23





#### Examencommissie:

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# Overview

### Background

- Machine learning & Neuromorphic computing
- Reservoir computing with signal-mixing cavities
- Photontorch: optimizing photonic circuits
- Neuromorphic computing with photorefractive crystals



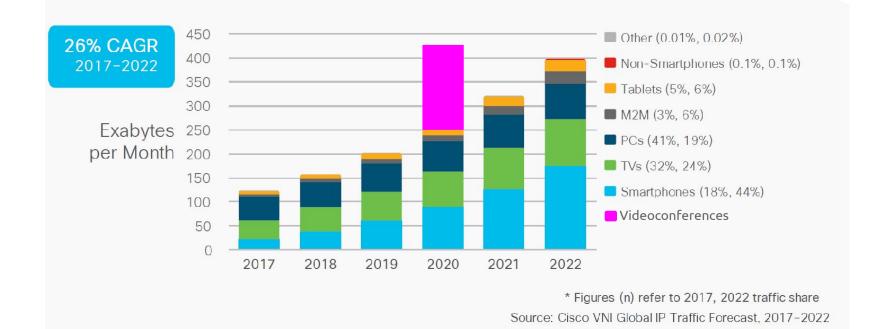
### Internet usage

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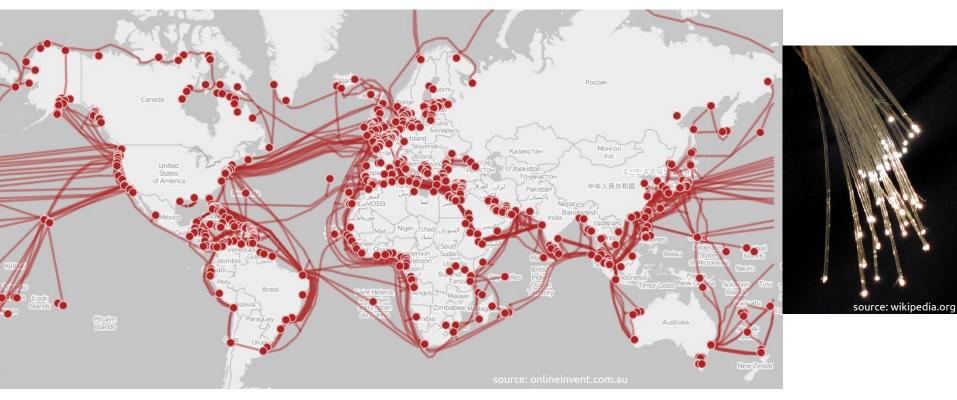
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embracing a better life



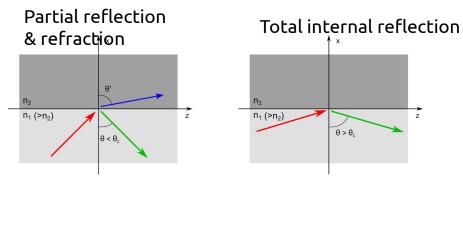
1 exabyte =  $10^{18}$  bytes = 1 million terabytes

### Data transmission is optical



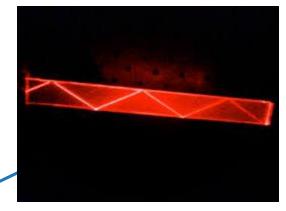


### Data transmission is optical











## Data **processing** is electrical

Digital Signal Processor (**DSP**):



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#### **Problem:**

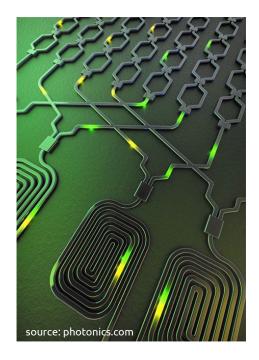
- Too costly
- Too power hungry

#### Main challenge:

- Increase bandwidth...
- ... while decreasing power consumption

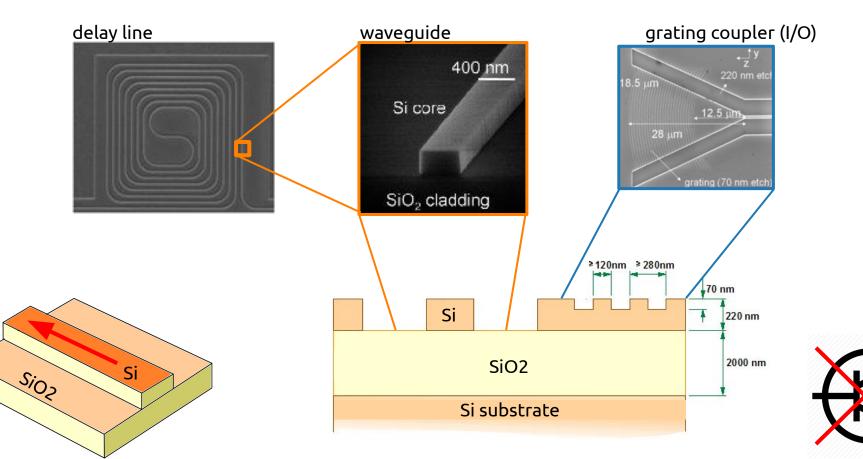
# **Optical** signal processors?

- Very high bandwidth
- Low energy consumption
- Highly parallel execution





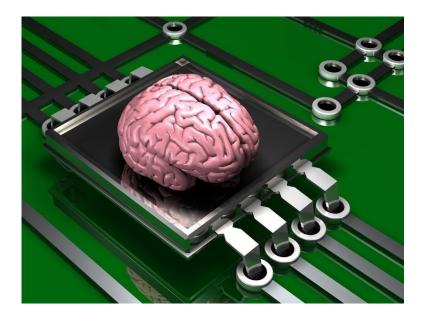
### Silicon photonics



## Neuromorphic computing

"brain-inspired"

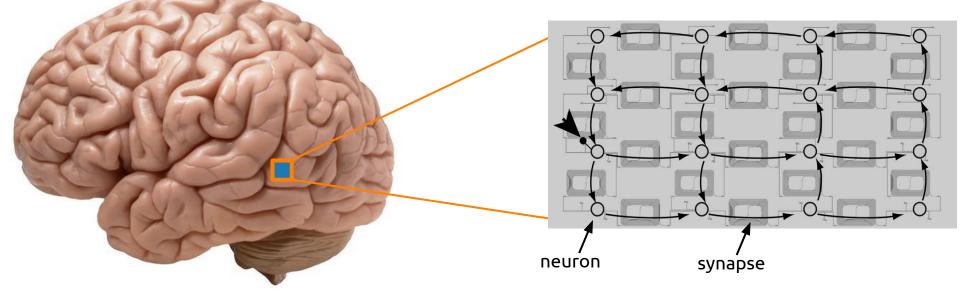






### Neuromorphic computing

"brain-inspired"

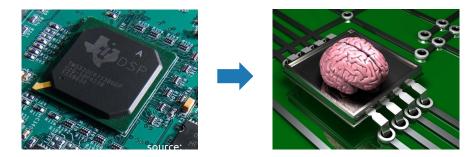




Goal

### Solve **telecom** related tasks

- Fast
- Efficiently



### By using smart **neuromorphic** photonic architectures



# Overview

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- Reservoir computing with signal-mixing cavities
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- Neuromorphic computing with photorefractive crystals



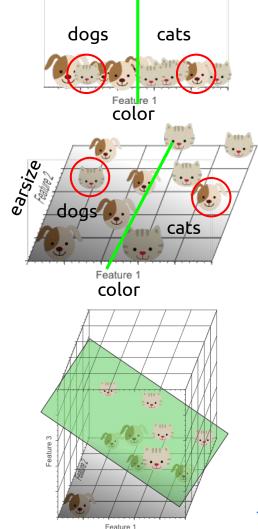
### Learn from examples





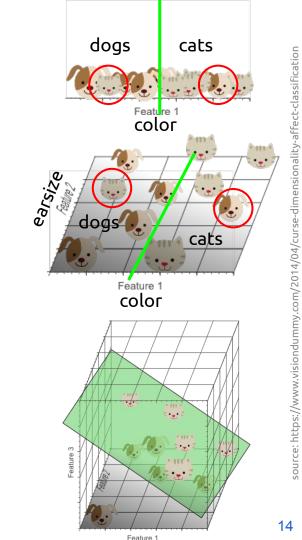






## Machine learning

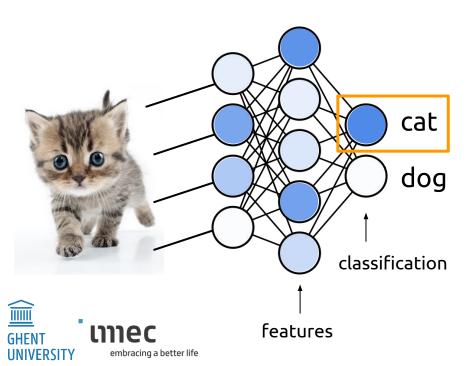
- Feature extraction
- Classification

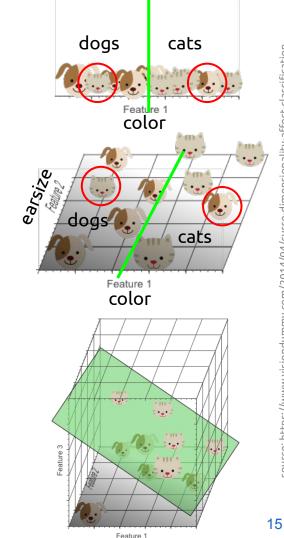




### Neural networks

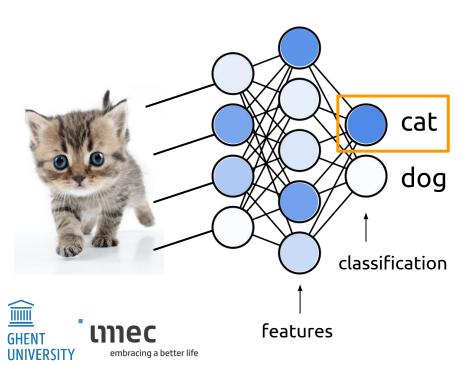
- Feature extraction
- Classification





### Neural networks

- Feature extraction
- Classification

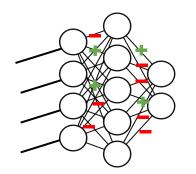


- Image recognition
- Facial recognition

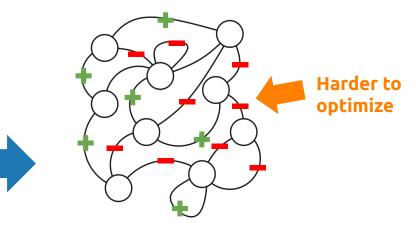
• ...

### Neural networks

#### (Deep) Neural Network



#### **Recurrent Neural Network**

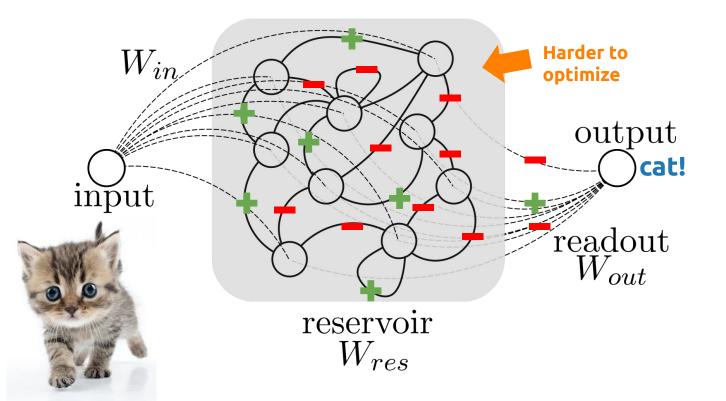


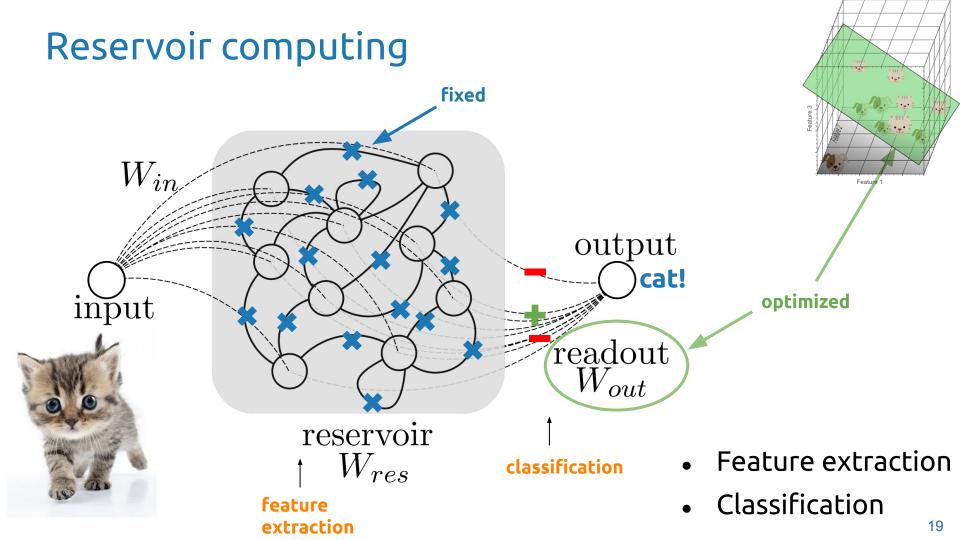
- Image recognition
- Facial recognition
- ...



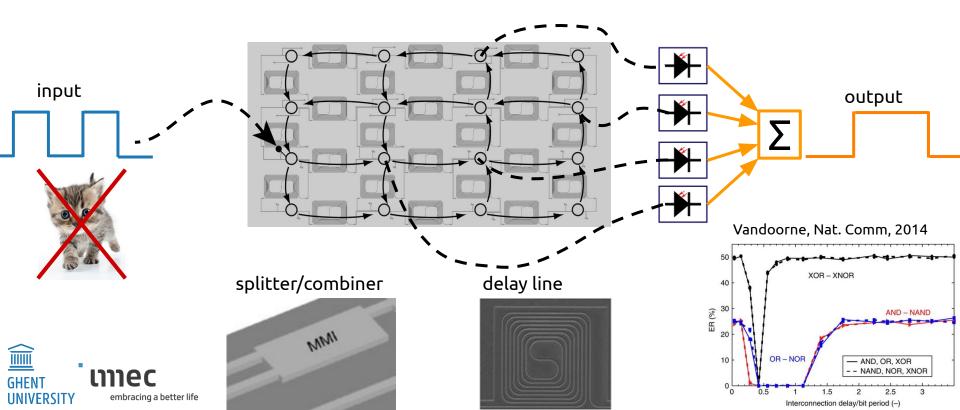
- Speech recognition
- Time series prediction
- Robot control
- ...

### **Recurrent Neural Network**



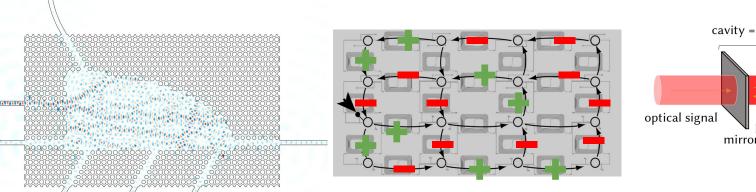


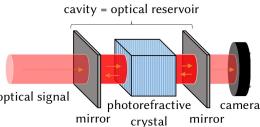
### Photonic Reservoir Computing



## Novel architectures for braininspired photonic computers

- Reservoir Computing with **signal-mixing cavities**
- Transition to completely **optimizable photonic circuits**
- Self-learning with photorefractive crystals





# Overview

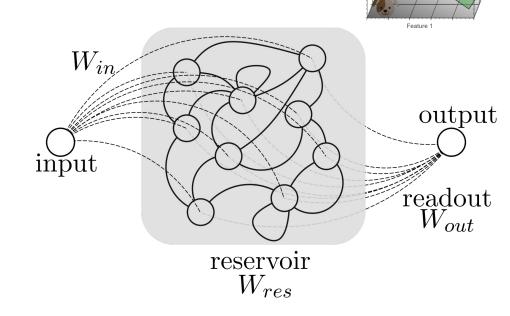
- Machine learning & Neuromorphic computing
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### **Reservoir Computing**

- preprocesses the input to a higher dimensional space
- has sufficient memory

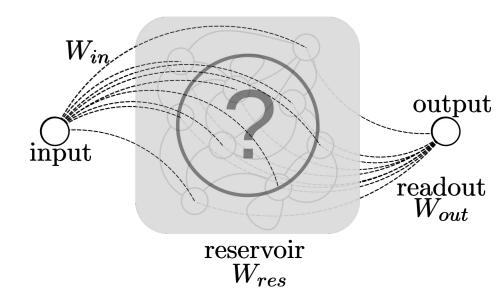




### Anything can be used as a reservoir!

As long as the system...

- preprocesses the input to a higher dimensional space
- has sufficient memory

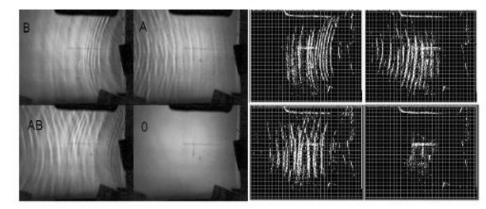




### Anything can be used as a reservoir!

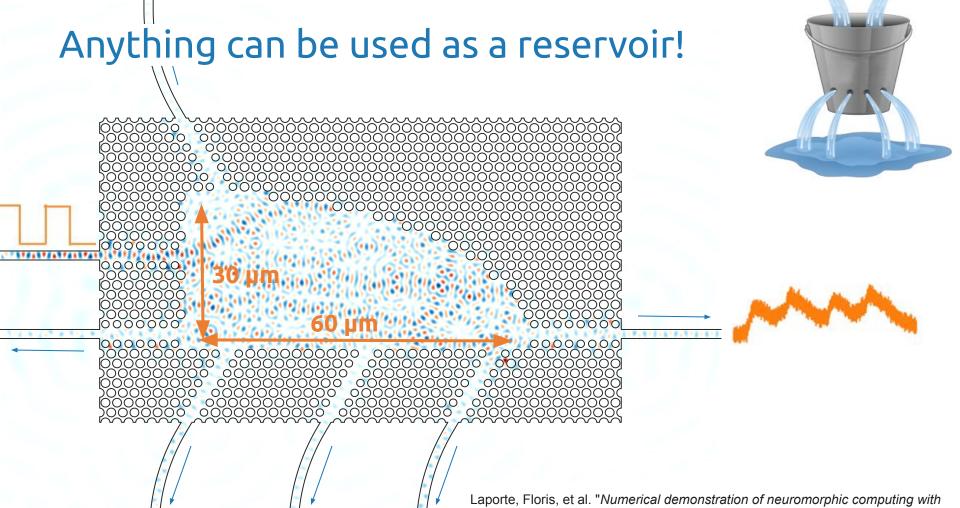
As long as the system...

- preprocesses the input to a higher dimensional space
- has sufficient memory



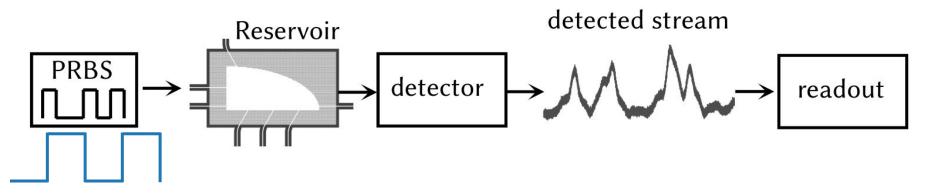
Fernando, C., & Sojakka, S. (2003, September). Pattern recognition in a bucket. In *European conference on artificial life* (pp. 588-597). Springer, Berlin, Heidelberg.





photonic crystal cavities." Optics express 26.7 (2018): 7955-7964.

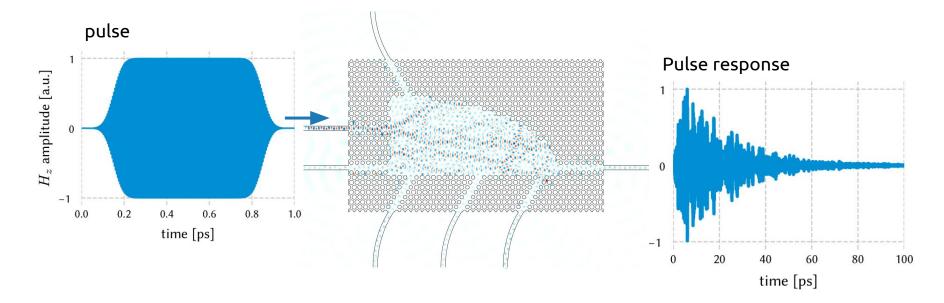
#### Ideally:



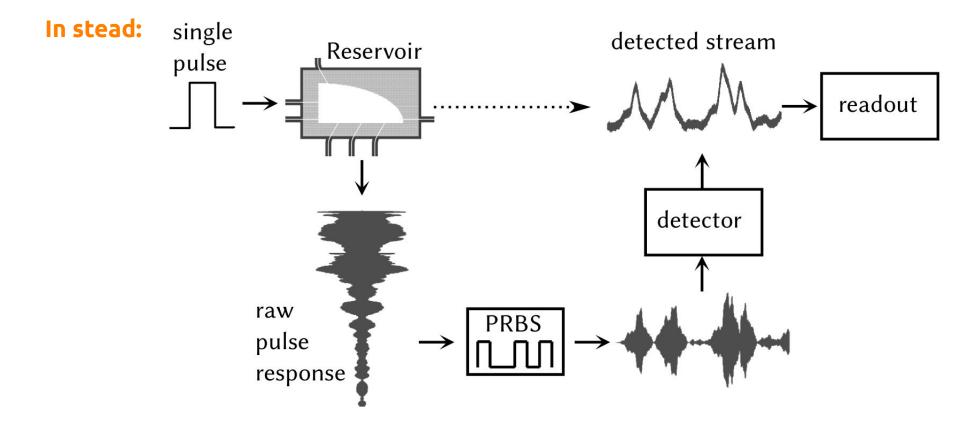
#### **Problem:** simulating a single bit takes about 24 hours.

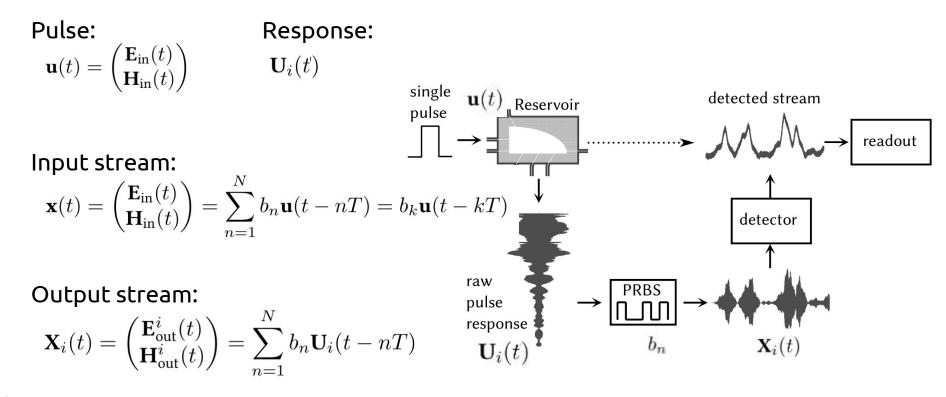


#### **Problem:** simulating a single bit takes about 24 hours.





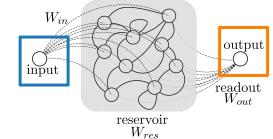


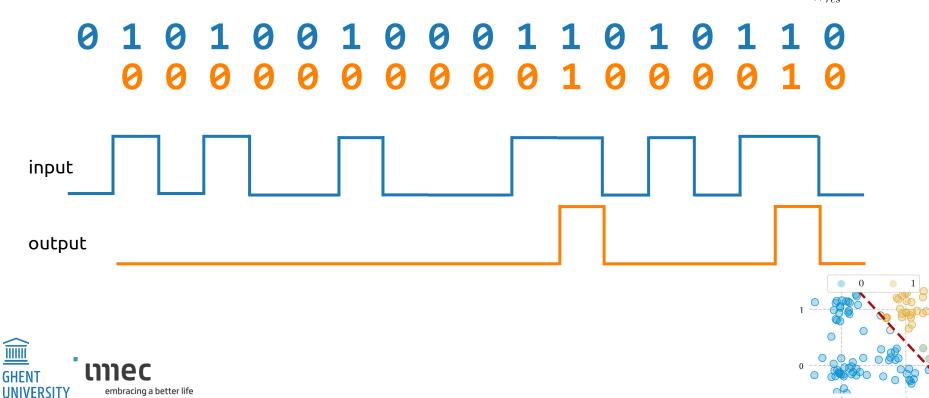




### Telecom tasks: AND

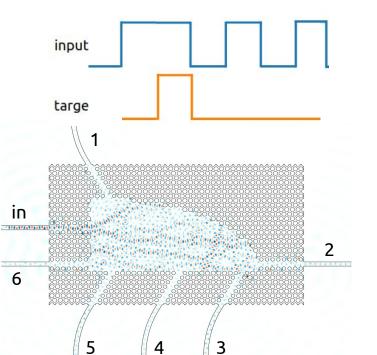
In telecom, we're working with bits:

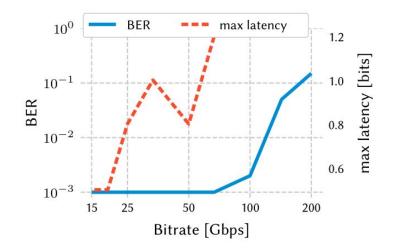




### AND Task (simulation)

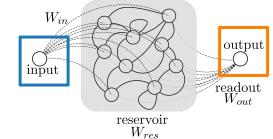
#### 

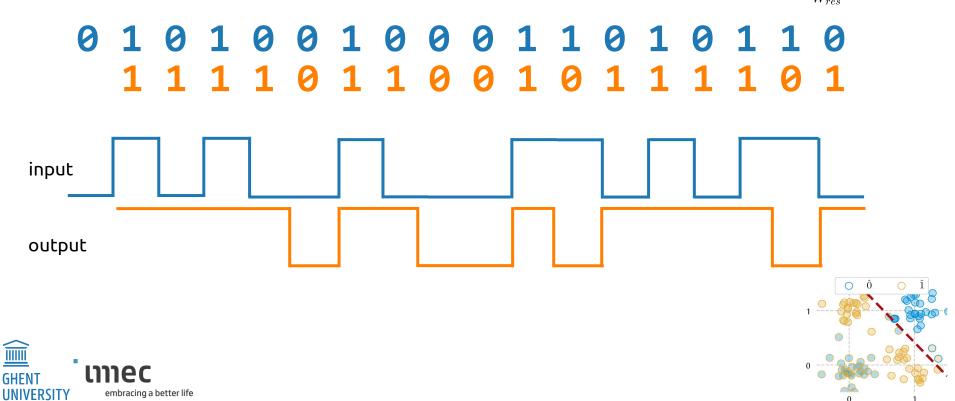


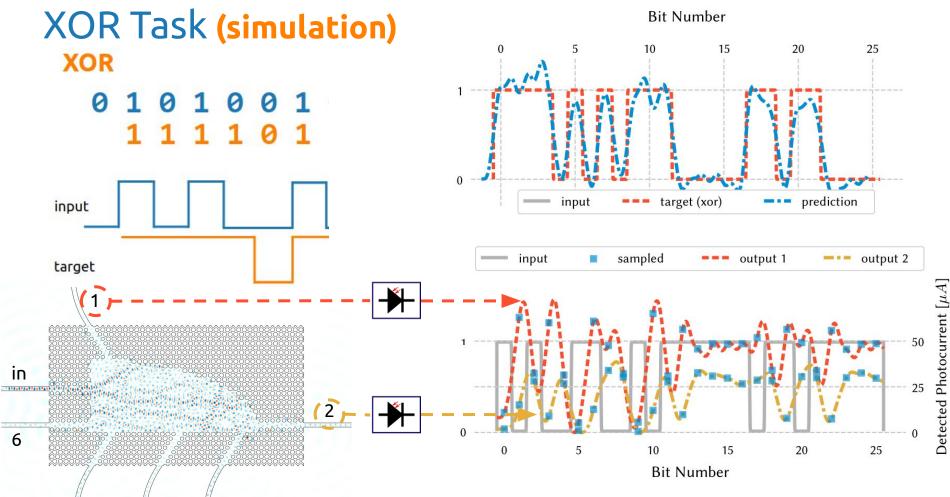


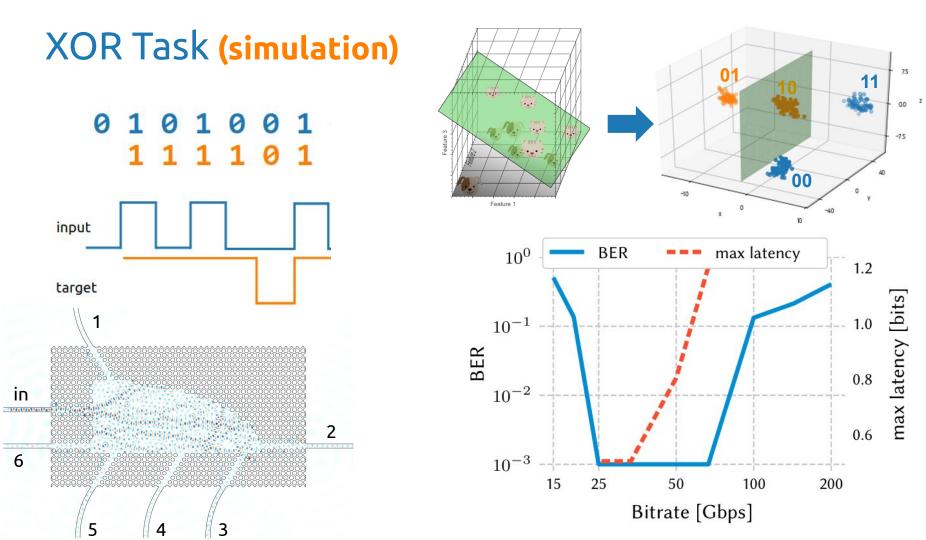
### Telecom tasks: XOR

In telecom, we're working with bits:



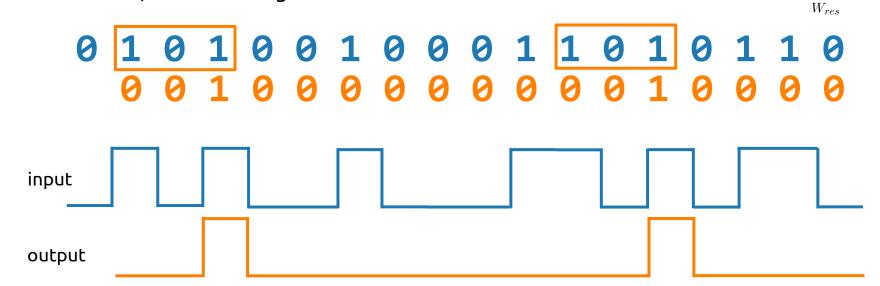






# Telecom tasks: Header Recognition

In telecom, we're working with bits:





output

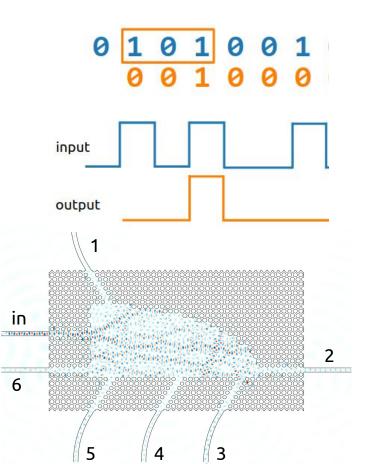
 $\stackrel{\rm readout}{W_{out}}$ 

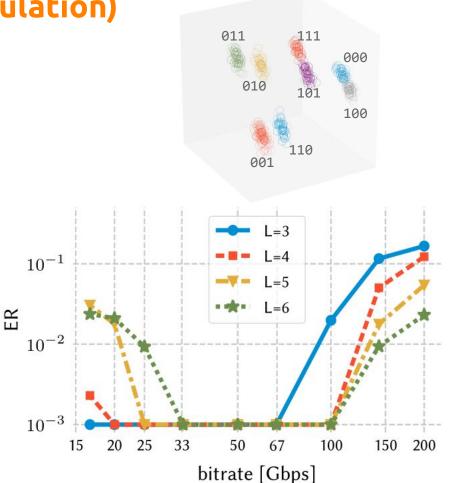
reservoir

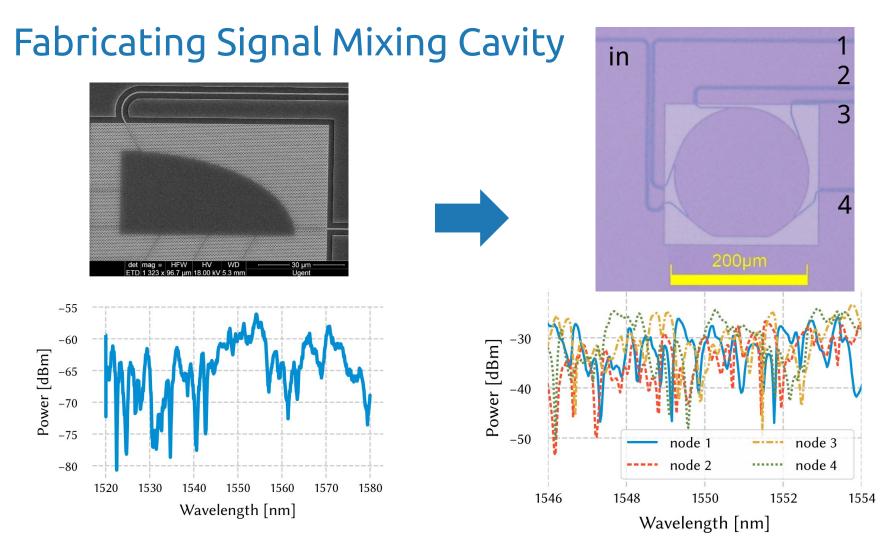
 $W_{in}$ 

input

### Header recognition (simulation)



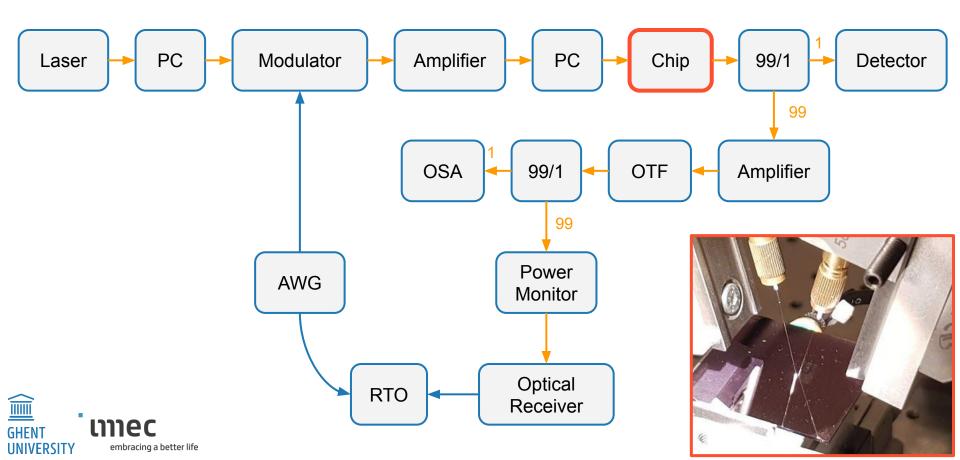


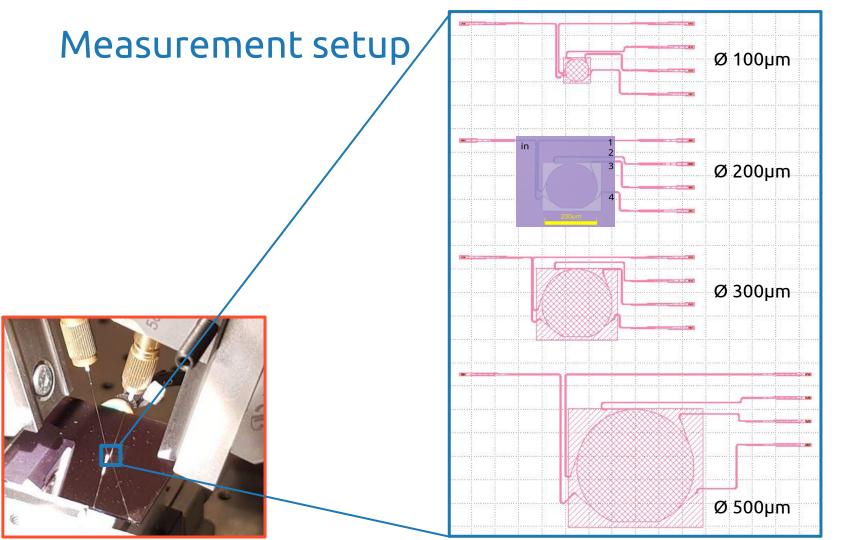


# Measurement setup



#### Measurement setup

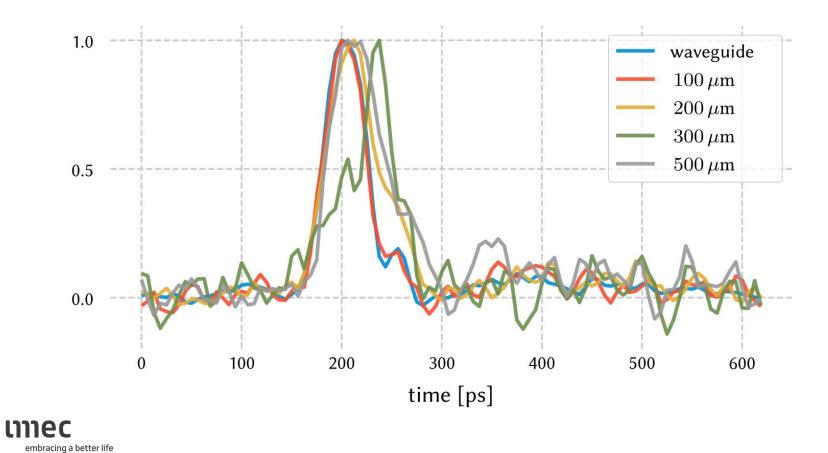




#### Fabricated cavities: pulse response

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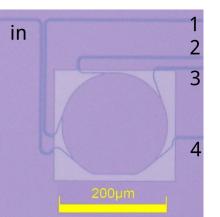
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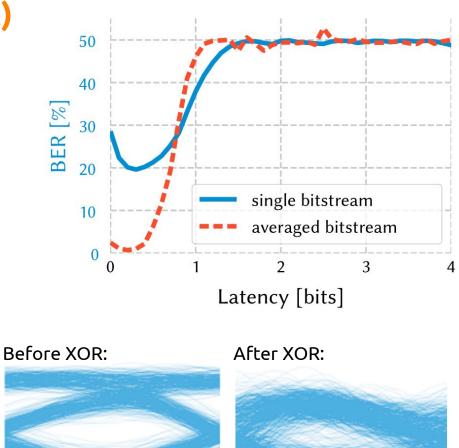


## XOR Task (measurement)

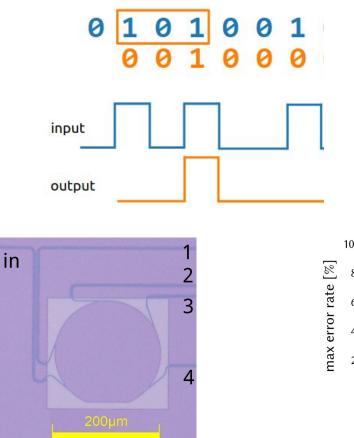
0 1 0 1 0 0 1 1 1 1 0 1

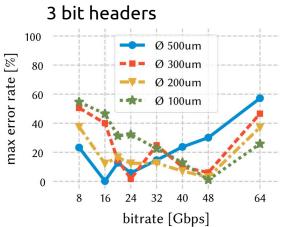


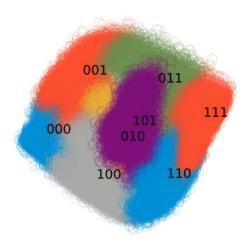




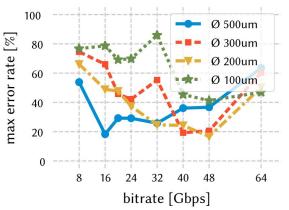
### Header recognition (measurement)







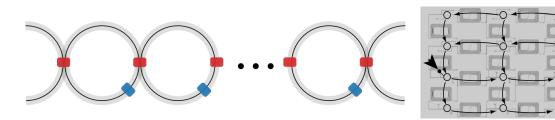
4 bit headers



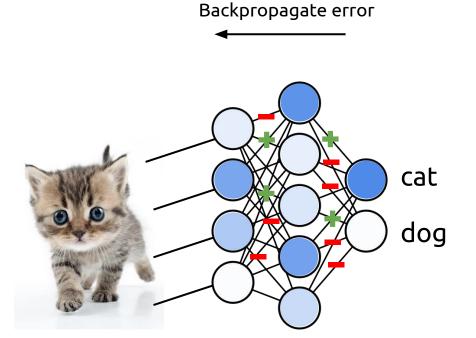
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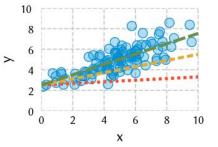


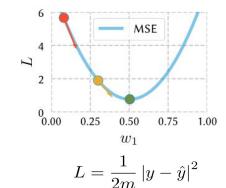
# Training neural networks: **backpropagation**





To minimize loss function

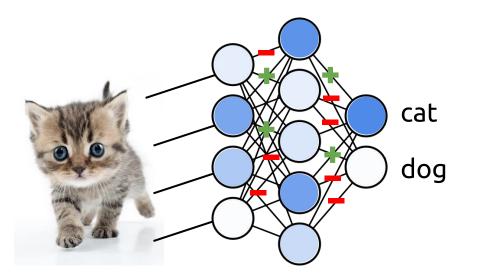




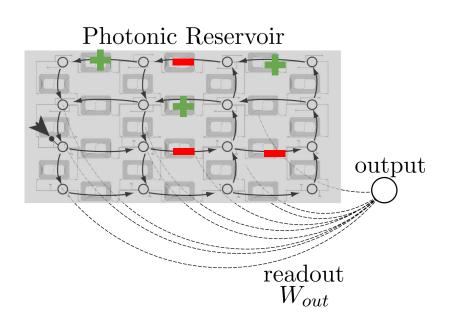
46



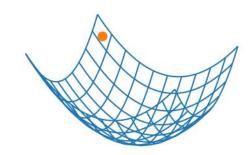
# Training photonic circuits with backpropagation?







## Photontorch: backpropagation through photonic circuits



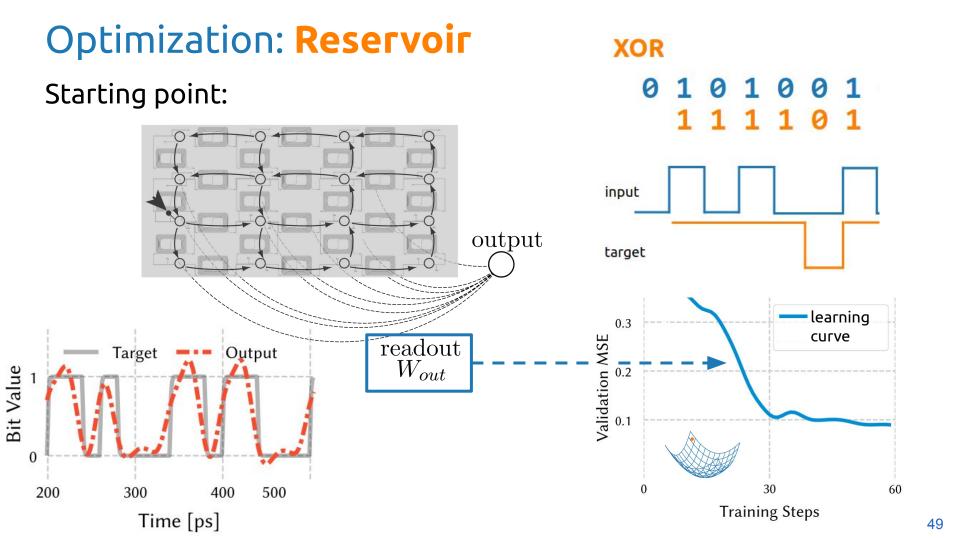
The simulator needs to keep track of the order of operations, i.e. the computation graph for the backward pass

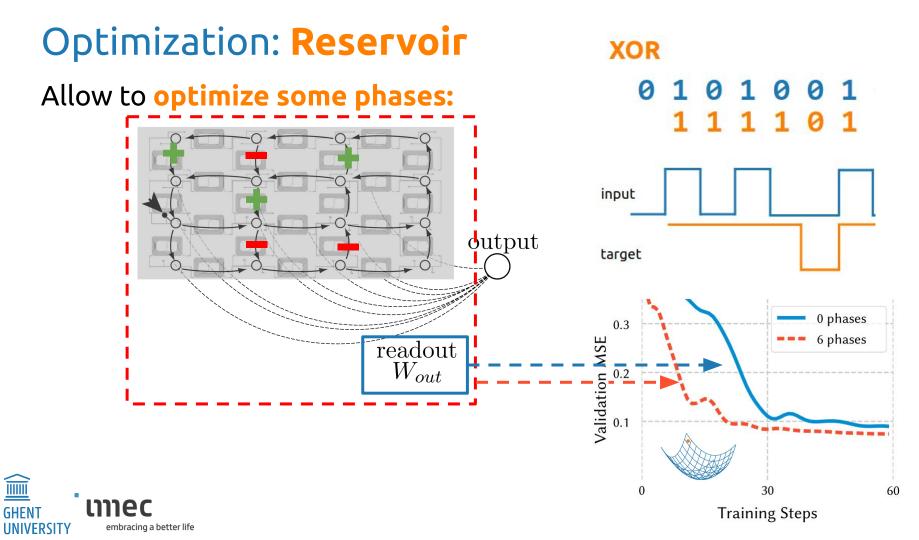


This requires a whole **new photonic simulator** 

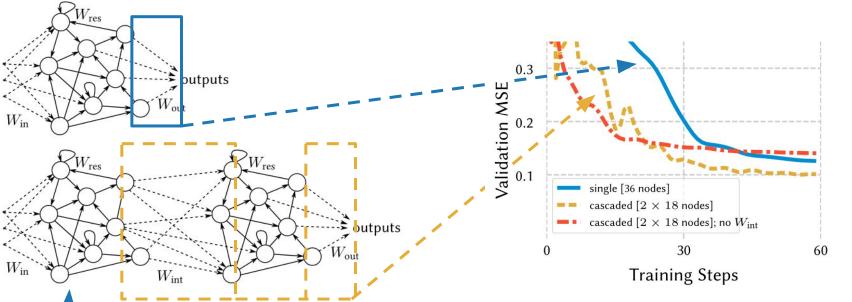




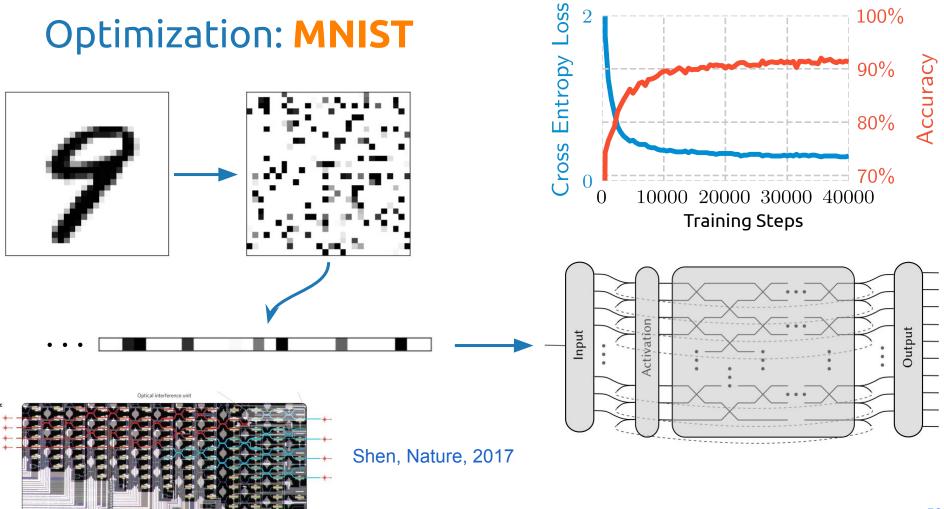




#### Optimization: Cascaded Reservoir



XOR of two bits with one intermediate bit

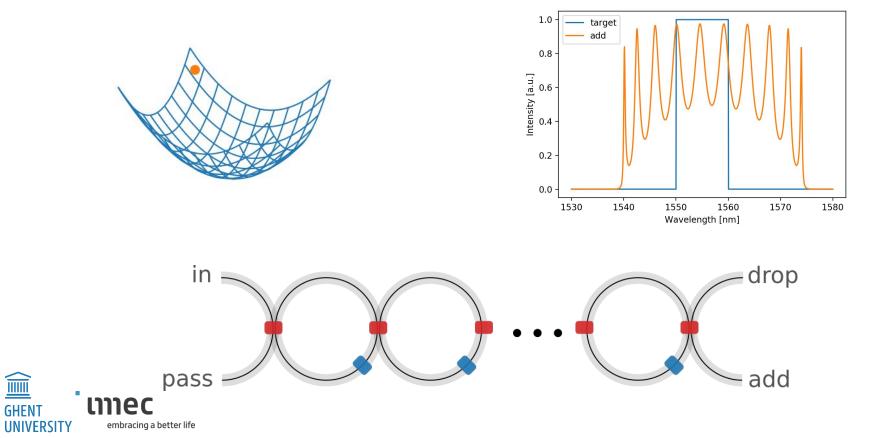


60 µm

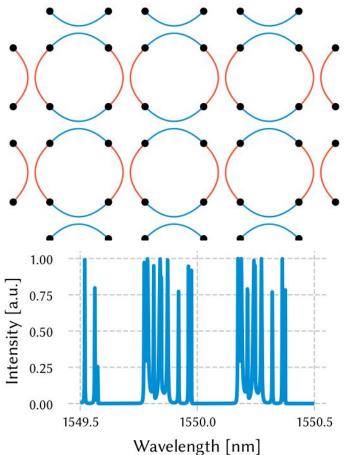
SU(4) core

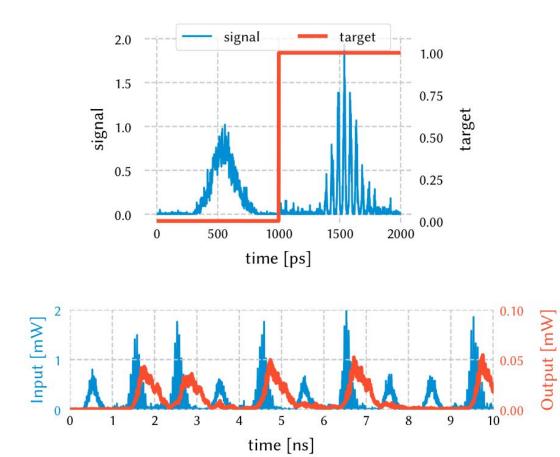
DMMC

#### Optimization: CROW



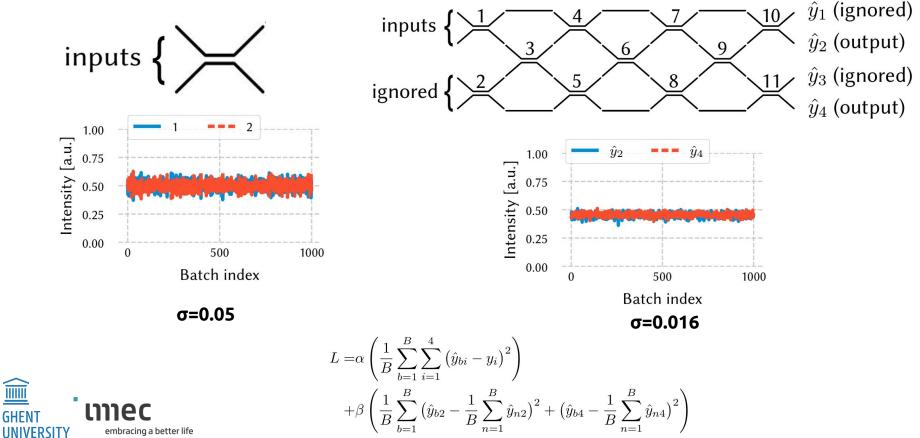
# Optimization: ring network



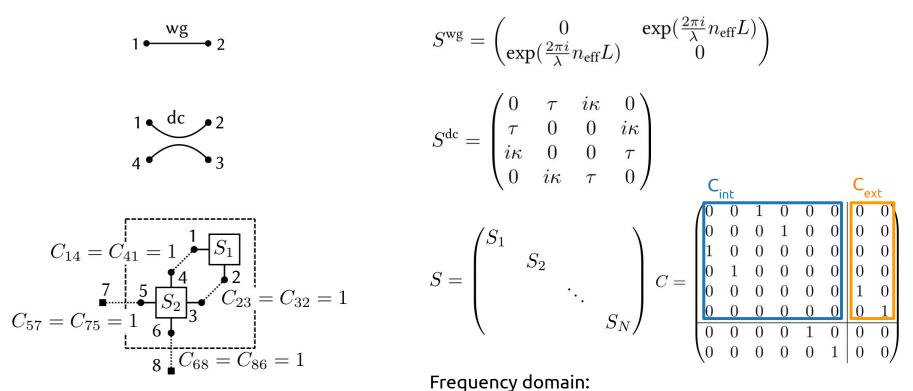


# Optimization: built-in tolerance

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#### Photontorch



 $S_{\text{circuit}} = C_{\text{ext}}^T S (I - C_{\text{int}} S)^{-1} C_{\text{ext}}$ 

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### Photontorch: very parallelizable

Frequency domain:

 $S_{\text{circuit}} = C_{\text{ext}}^T S (I - C_{\text{int}} S)^{-1} C_{\text{ext}}$ 

Time domain:

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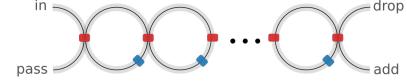
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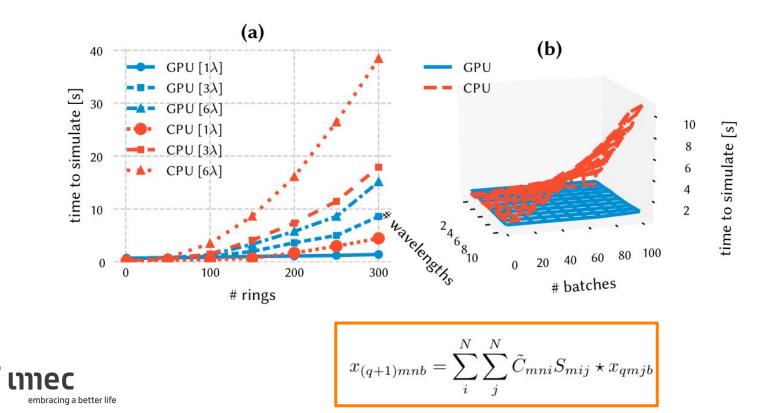
- Ideally: FT(S<sub>circuit</sub>) ----> VF, IIR, FIR, ... Approximation: ullet
- •



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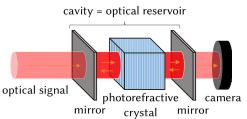




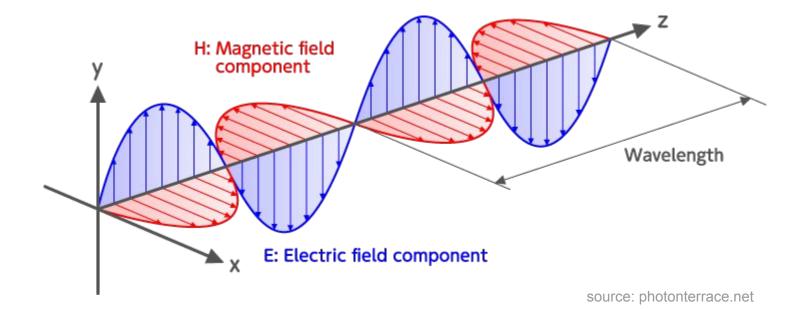
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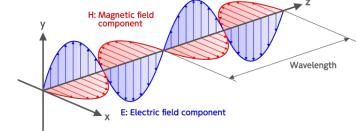


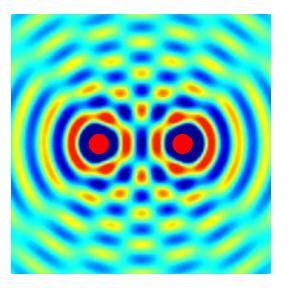
# Light is an electromagnetic wave





#### Interference

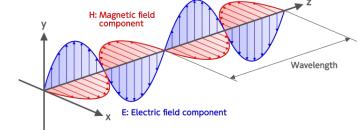


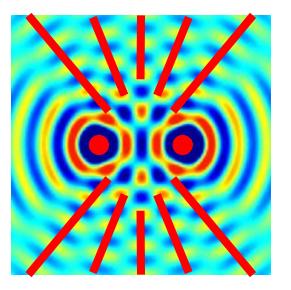


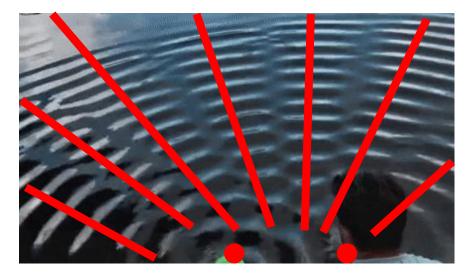




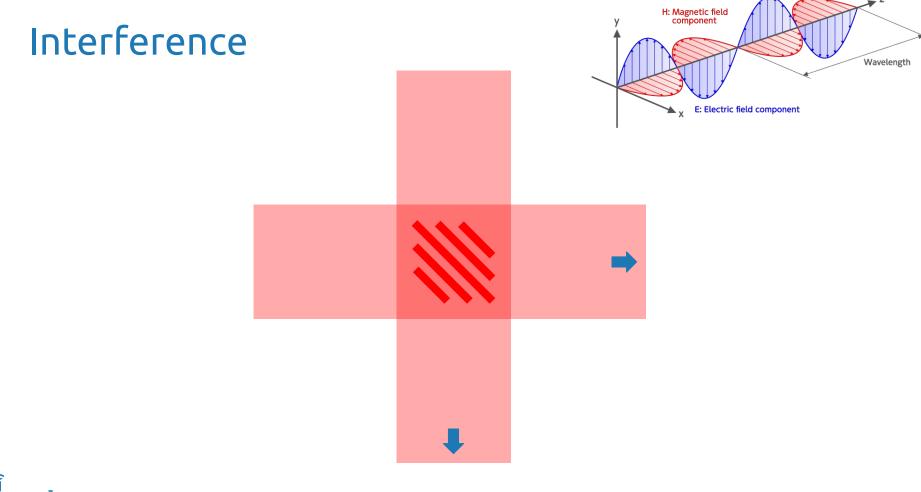
#### Interference



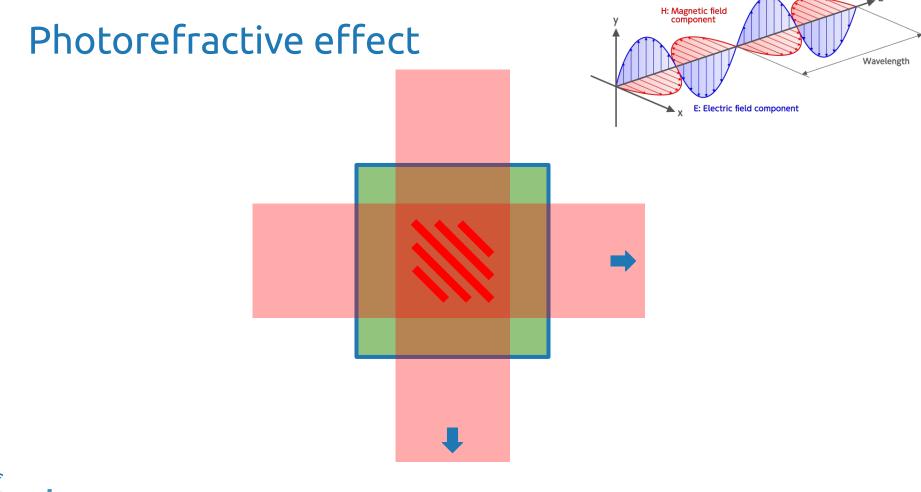








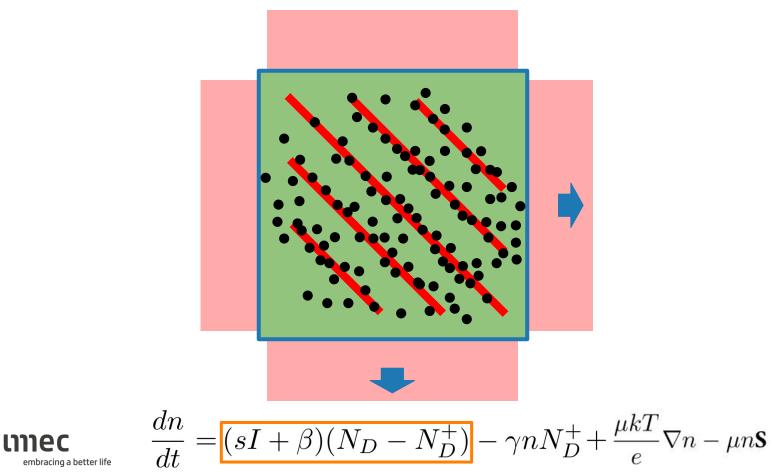




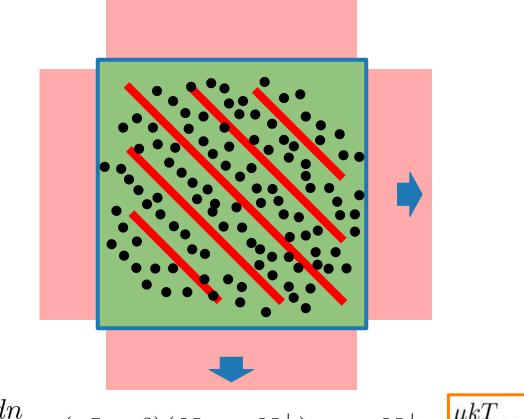


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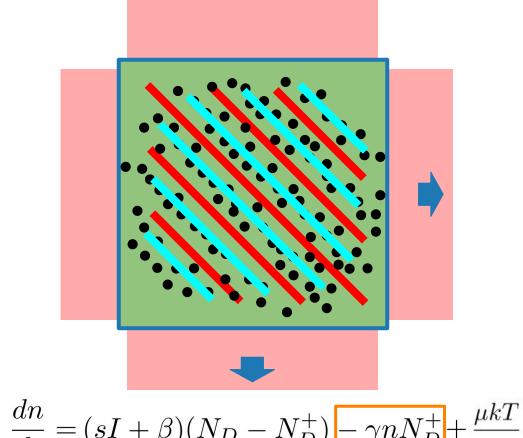


65





$$\frac{dn}{dt} = (sI + \beta)(N_D - N_D^+) - \gamma nN_D^+ + \frac{\mu kT}{e}\nabla n - \mu n\mathbf{S}$$



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$$\frac{dn}{dt} = (sI + \beta)(N_D - N_D^+) - \gamma n N_D^+ + \frac{\mu kT}{e} \nabla n - \mu n \mathbf{S}$$

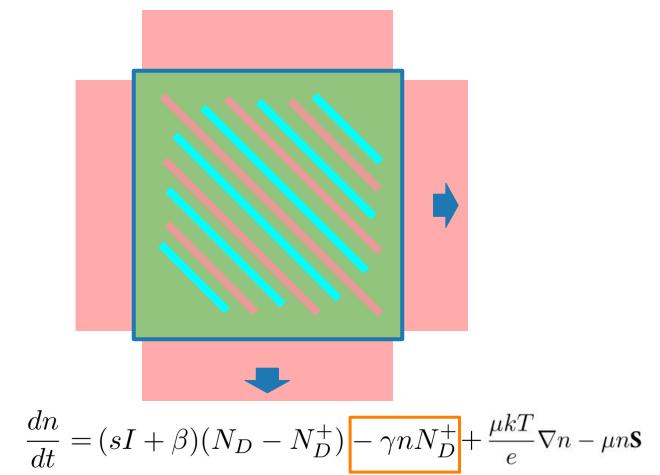
67

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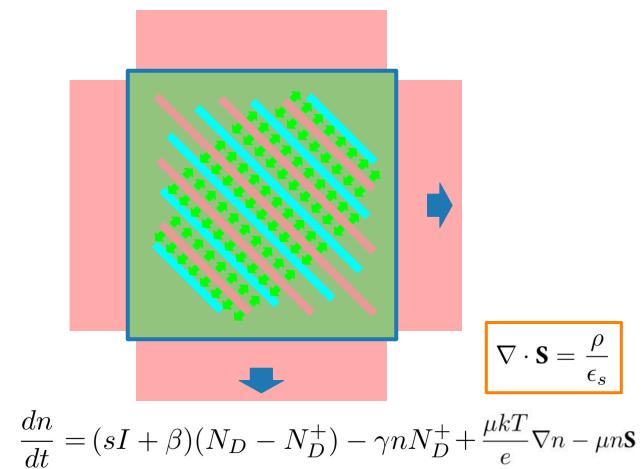


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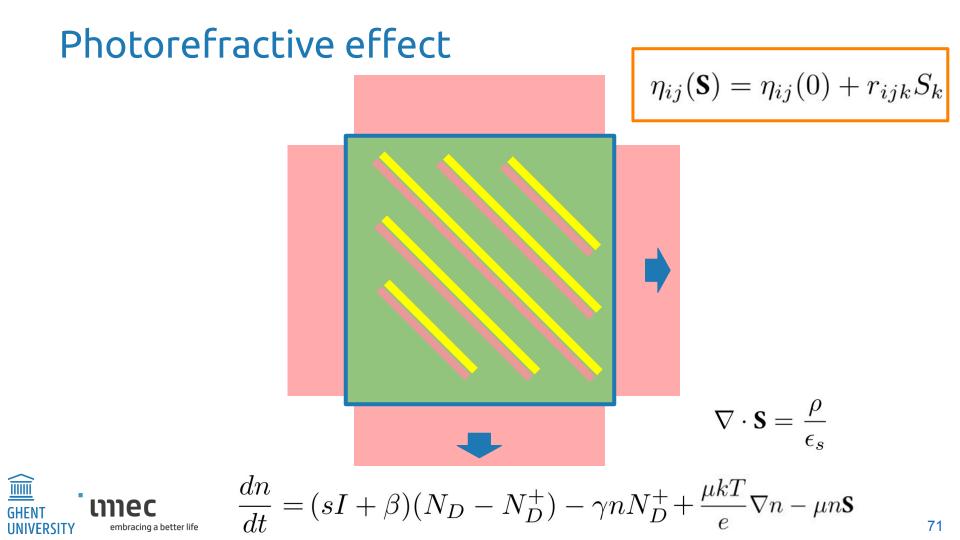
umec

$$\eta_{ij}(\mathbf{S}) = \eta_{ij}(0) + r_{ijk}S_k$$

$$\nabla \cdot \mathbf{S} = \frac{\rho}{\epsilon_s}$$

$$\frac{dn}{dt} = (sI + \beta)(N_D - N_D^+) - \gamma nN_D^+ + \frac{\mu kT}{e}\nabla n - \mu n\mathbf{S}$$
70

70



# Bringing it all together

#### Kukhtarev equations:

$$\frac{dn}{dt} = \frac{dN_D^+}{dt} + \nabla \cdot \mathbf{J}$$
$$\frac{dN_D^+}{dt} = (sI + \beta)(N_D - N_D^+) - \gamma nN_D^+$$
$$\mathbf{J} = \frac{\mu kT}{e} \nabla n - \mu n\mathbf{S}$$

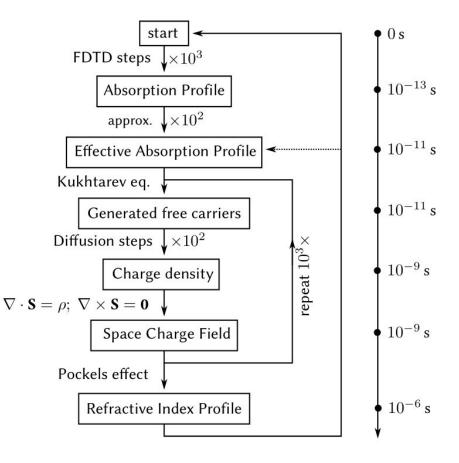
Diffusion Equation:  $\frac{\partial n}{\partial t} = \left. \frac{\partial n}{\partial t} \right|_{\text{diff}} + \left. \frac{\partial n}{\partial t} \right|_{\text{drift}} = D\nabla^2 n - \nabla \cdot \mathbf{F}$ 

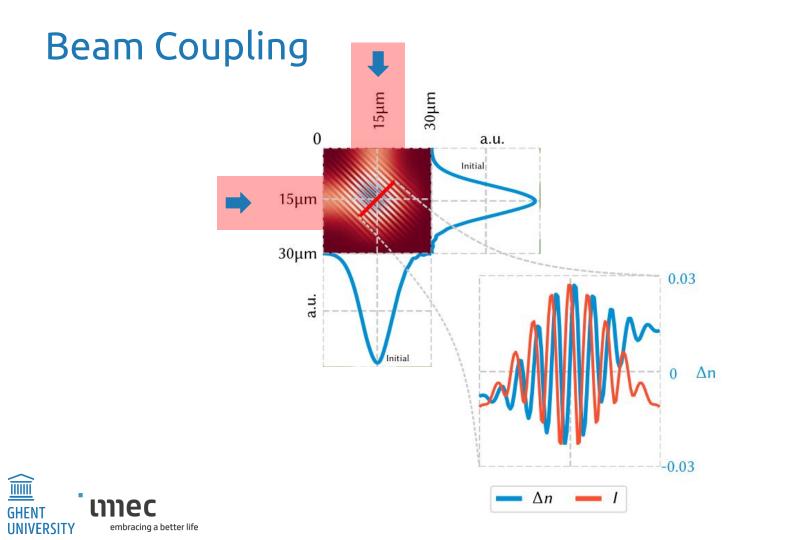
Maxwell equations:

 $abla \cdot \mathbf{S} = rac{
ho}{\epsilon_s}$   $abla \times \mathbf{S} = \mathbf{0}$ 

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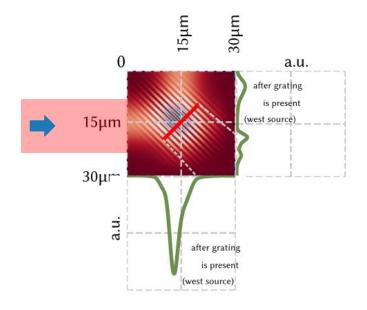
UNIVERSITY





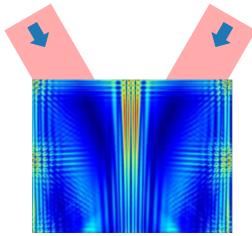


## Beam Coupling





# Self learning with photorefractive crystals



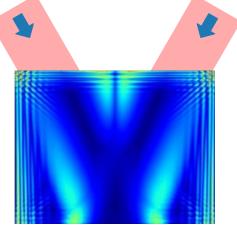
**Purely Interfering** (same bits arrive at the same time)

GHEN

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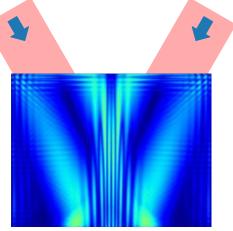
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**Alternating** (opposite bits arrive at the Alternating times)

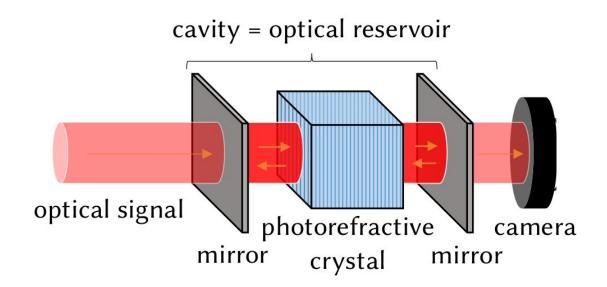




#### Random

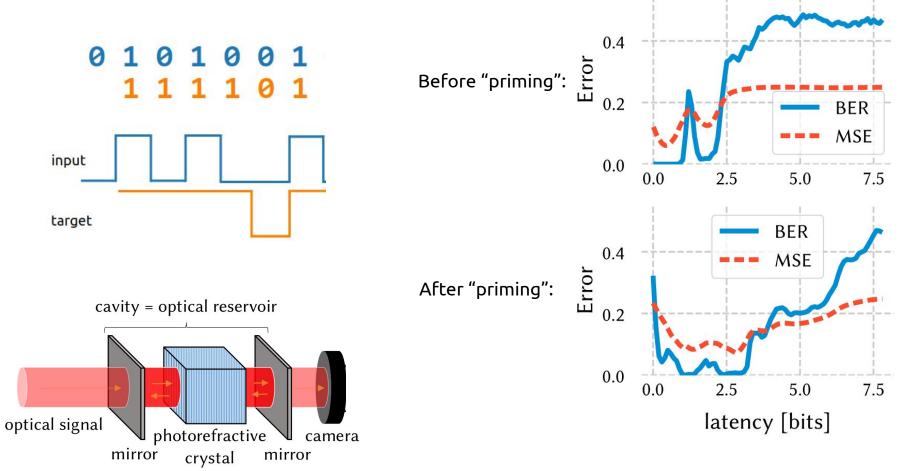
(two random bitstream, Sometimes interfering, Sometimes alternating)

## Self learning with photorefractive crystals





#### **XOR Task**



# Conclusions



# Conclusions

- Reservoir computing with **on-chip cavities** seems to be a promising alternative to traditional node-based designs:
  - Benchmark tasks like **HREC and XOR show promising results**, both in simulation and experiments.
- Photontorch shows the viability of optimizing photonic circuits through backpropagation
  - Completely **new way of optimizing** photonic circuits
  - Very parallelizable (→ Fast)
    - wavelength multiplexing
    - waveform multiplexing (batched execution)
- Self-reorganization in photorefractive crystals, such as priming the crystal might help for neuromorphic computing schemes beyond reservoir computing such as self-learning