SWIR InGaAs/GaAsSb type-II quantum well photodetectors and spectrometers integrated on SOI

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Abstract

The short-wave infrared wavelength range (2-3 μ m) is attractive for applications in gas sensing and nextgeneration communication systems. Photodetectors and wavelength (de)multiplexers are key components that have to be developed for these systems. In this contribution, we report the integration of InGaAs/GaAsSb type-II quantum well photodetectors and spectrometers on the silicon photonics platform. In this photodetector epitaxial layer stack, the absorbing active region consists of 6 periods of W-shaped quantum wells, which can also be used to realize lasers. The efficient coupling between silicon waveguides and quantum well photodetectors is realized by tapered III-V waveguides. The photodetectors have a very low dark current of 12 nA at -0.5 V bias at room temperature. The devices show a responsivity of 1.2 A/W at 2.32 μ m wavelength, and higher than 0.5A/W over the 2.2-2.4 μ m wavelength range. On the silicon-on-insulator platform we also demonstrate high performance short-wave infrared spectrometers. 8-channel spectrometers in the 2.3-2.4 μ m range with a resolution of 5nm and 1.4nm are demonstrated, showing a cross-talk below -25 dB and an insertion loss lower than 3 dB.

Keywords: silicon photonics, short-wave infrared, photodetectors, arrayed waveguide gratings, heterogeneous integration¹

1. INTRODUCTION

The 2-3 µm short-wave infrared (SWIR) wavelength range is of great interest for spectroscopic sensing as a lot of important industrial gases have strong absorption signatures in this wavelength range¹. For example, the wavelength range around 2.3 µm offers the first water absorption free spectral window for CO detection. Besides, this wavelength range has also shown potential for next generation communication systems because of recently improved transmission and amplification capabilities of hollow-core photonic-bandgap fibres². In recent years, silicon photonics has been attracting a lot of interest for the SWIR wavelength range since it can take advantage of standard complementary metal-oxide-semiconductor (CMOS) process technology to fabricate integrated photonic devices^{3,4}. Among all of the components in integrated photonics system, arrayed waveguide gratings (AWGs) are widely used in optical communication and spectroscopy⁵. AWGs implemented in silicon photonicsare much more compact than the devices based on low-index-contrast material systems. AWG spectrometers operating around 2.2 µm were demonstrated based on a silicon-on-insulator (SOI) strip waveguide structure⁶. The low resolution (4.8 nm) and high resolution (1.6 nm) AWG spectrometers have a crosstalk of -12 dB and -16 dB, and insertion loss of 2.1 dB and 4 dB, respectively. Comparing with an SOI AWG operating at telecommunication wavelengths, the performance of the 2 µm wavelength range SOI AWGs should be further improved⁷. In this paper, we demonstrate low insertion loss and low crosstalk AWGs based on a shallow-etched SOI waveguide platform. The low resolution (5 nm) and high resolution (1.4 nm) AWG spectrometers have a crosstalk of -30 dB and -25 dB, and insertion loss of 2.5 dB and 2.8 dB, respectively.

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Photodetectors are also one of the key components that should be developed for silicon photonics systems in the 2-3 µm SWIR wavelength range. Epitaxial growth of strain-relaxed GeSn on silicon can be used to achieve cutoff wavelengths beyond 2 μm^8 . However, this material system has not yet been demonstrated to be integrated with silicon waveguide circuits. Recently, monolithic silicon photodetectors with responsivity of 0.3 A/W in the 2 µm wavelength range were demonstrated based on defect engineered silicon⁹. Integration of III-V material on silicon photonics platform by adhesive bonding or molecular bonding is an effective technology to integrate active opto-electronic devices on the silicon platform¹⁰. There are many demonstrations of heterogeneously integrated InP-based lasers and photodetectors on silicon waveguide circuits for the 1.3 µm / 1.55 µm optical communication wavelength range, however, there are very few demonstrations beyond 2 µm wavelength. For the 2-3 µm wavelength range, GaSb-based type-I heterostructures can be used to make active devices. However, the heterogeneous integration process and device processing yield of GaSb-based devices lags behind that of InP. Alternatively, the emission and detection cut-off wavelength of InP-based type-II heterostructures can cover the 2-3 µm wavelength range¹¹. Heterogeneous integration of InP-based type-II devices can take advantage of the mature InP heterogeneous integration processes. In this paper, we report the integration of InP-based type-II InGaAs/GaAsSb quantum well photodetectors on SOI. At room temperature, the heterogeneously integrated photodetectors show a dark current of 12 nA at a reverse voltage of 0.5 V, and a responsivity of 1.2A/W at 2.32 µm wavelength.

2. SWIR AWG SPECTROMETER

The fabrication of passive SOI waveguide circuits are carried out on 200 mm silicon wafers in imec's CMOS pilot line. The SOI wafer consists of a 400 nm thick silicon device layer on a 2 μ m buried oxide layer. 850 nm wide waveguides are designed for the propagation of 2.35 μ m wavelength light. The silicon waveguide pattern is defined by 193 nm deep-ultraviolet (DUV) lithography. Then inductively-coupled plasma (ICP) dry etching is used to etch 180 nm deep silicon rib waveguide with smooth sidewalls and a vertical profile. Afterwards, a layer of SiO₂ is deposited on the SOI wafer and planarized by a chemical mechanical polishing (CMP) process.



Figure 1. Microscope image of the low resolution AWG.

Detailed information about the principle of AWG design can be found in [5]. A low resolution AWG with channel spacing of 5 nm and a high resolution AWG with channel spacing of 1.4 nm are designed for the 2.3-2.4 μ m wavelength range in this work. Both AWGs have 8 channels for using. For these AWGs, we choose the following parameters for design: the number of arrayed waveguides N_{wg}= 32, the length of the free propagation region (FPR) L_{FPR}= 108 μ m, the diffraction order m = 36 for the low resolution AWG and 130 for the high resolution AWG. Figure 1 shows the microscope image of the low resolution AWG. The footprint of the low resolution and high resolution AWG is 0.45 mm² and 1.5 mm², respectively. This small device size can enable a SWIR miniature spectroscopy system based on the silicon photonics platform.



Figure 2. Transmission spectrum of the low resolution (a) and high resolution (b) AWG.

The AWG performance including insertion loss and crosstalk is characterized by measuring the transmission spectrum of every channel. TE-polarized light from a tunable SWIR laser (IPG Cr:ZnSe solid state laser) is coupled to a standard single mode fiber and then split by a 99/1% splitter. 1% of the input power is coupled to a photodetector, which can be used as a power reference. 99% of the input power is coupled to the AWG through a grating coupler and then collected by an optical spectrum analyzer (Yokogawa AQ6375). The measured transmission spectra of AWGs are normalized to a reference waveguide, and are shown in Figure 2 (a) and (b). From this figure, it is can be seen that the average insertion loss is around 2.5 dB and the crosstalk level is -30 dB for the low resolution AWG. The high resolution AWG has an average insertion loss of about 2.8 dB and crosstalk level of -25 dB. The insertion loss and crosstalk of these AWGs are much lower than previously demonstrated 2 µm wavelength AWGs based on strip waveguides. The performance is comparable with state-of-the-art SOI AWGs operating at telecommunication wavelengths.

3. HETEROGENEOUS TYPE-II QUANTUM WELL PHOTODETECTORS

The heterogeneously integrated photodetectors consist of InP-based type-II quantum wells bonded to an SOI waveguide circuit as shown in figure 3 (a). In order to efficiently couple the light from the silicon waveguide to III-V active region, both the III-V waveguide and silicon waveguide are tapered. Figure 3(b) shows the schematic of the top view of the heterogeneously integrated devices. The length of the III-V waveguide is 150 μ m, tapered from 1 μ m to 3.5 μ m. The silicon waveguide underneath is a 400 nm thick rib waveguide etched 180 nm deep, tapered from 6 μ m to 3 μ m. The light is confined in the silicon waveguide in the beginning of the double taper section, and then gradually couplse up to III-V waveguide when the photodetector mesa goes wider. Detailed information about the design of the photodetector can be found in [12].



Figure 3. Schematic of the cross-section (a) and top view (b) of the InP-based type-II quantum well photodetector integrated on an SOI waveguide circuit.

The III-V epitaxial structure of the photodetector consists of a p-InGaAs contact layer, a p-InP cladding layer, an absorbing active region sandwiched between two separate confinement heterostructure (SCH) layers, and an n-InP contact layer. The active region consists of six periods of a "W"-shaped quantum well structure, each separated by 9 nm tensile strained GaAsSb. Each quantum well consists of one 2.9 nm GaAsSb layer confining the holes, surrounded by two 2.6 nm electron confining InGaAs layers. Specific information on the active region design can be found in Ref. 11, as it has been used for the realization of 2.4 µm wavelength lasers. This III-V structure is grown on an InP substrate by a molecular beam epitaxy (MBE) system and then transferred to the patterned silicon wafer by adhesive bonding with a 100 nm thick BCB layer. Afterwards the InP substrate is removed by a HCl solution using InGaAs as etch stop layer.

Figure 4(a)-(h) shows the process flow of the heterogeneously integrated photodetector. After bonding and substrate removal, wet-etching of the p-InP and active region is used to process the photodetector mesas on the III-V membrane. The first process is to define a tapered waveguide with 1 μ m taper tip in a SiN_x hard mask layer. Then the InGaAs contact layer is etched by ICP and the *p*-InP cladding layer is etched using a 1:1 HCl:H₂O solution. This anisotropic HCl wet etching of InP can generate undercut structures when the mesa is oriented along the [01-1] direction, which reduces the lithographic requirements of the tapered waveguides. For the lithography pattern with 1 um taper tip on the hard mask, the bottom width of the p-InP after HCl etching is around 200 nm wide. After *p*-InP etching, subsequent wet-etching of the quantum well layers to the n-InP layer is performed by a H₃PO₄: H₂O₂: Citric Acid: H₂O 1:1:20:70 solution. After metallization, DVS-BCB solution is spin coated on the sample to passivate the devices. After curing at 250°C for 1.5h, the BCB layer is etched by reactive ion etching (RIE) for the opening of the p-contact and n-contact. In the end, Ti/Au is deposited on the n-contact and p-contact as probe pad. Fig. 4(i) shows a scanning electron microscope (SEM) image of the device cross section.



Figure 4. (a)-(h): Process flow for the hybrid InP-based type-II quantum photodetector, (i) SEM image of the cross section of the fabricated device.

The electrical characteristics of the heterogeneously integrated photodetectors were measured. Figure 5 shows the I-V curve at room temperature. The measured dark current under reverse bias of 0.5 V is 12 nA (resulting in a current density of 0.17 mA/cm²). As the reverse bias is increased, the dark current increases gradually but remains lower than 100 nA at a reverse bias of 3.5 V. This is 3 orders of magnitude lower than heterogeneously integrated GaSb-based photodetectors previously demonstrated by our group, enabling a higher sensitivity. This low dark current is attributed to the well-developed InP processes and timely BCB passivation of the photodetector mesa. Besides, passivation of the p-InP cladding layer using SiN_x also can enable low dark current.



Figure 5. I-V curve of the heterogeneously integrated photodetector without light input. The inset picture shows the dark current of the heterogeneously integrated photodetector operating from -1 V to 0V.

In the optical measurements, TE-polarized light is coupled to the silicon waveguide circuit by a 20 μ m ×20 μ m grating coupler with a period of 1 μ m. The measured peak coupling efficiency of the grating coupler is around - 9.5dB at 2.35 μ m with 150 nm 3 dB bandwidth as shown in Figure 6(a). Figure 6(b) depicts the dependence of responsivity on reverse bias for a wavelength of 2.3 μ m and 2.4 μ m. It can be found that the responsivity at 2.4 μ m is much more strongly dependent on reverse voltage than 2.3 μ m. This can be attributed to strong electroabsorption at wavelengths close to the band gap wavelength of the active region. Figure 6(c) shows the optical responsivity as function of the input laser wavelength (2.2 -2.42 μ m) under reverse bias of 0 V and 0.5 V. It can found that the heterogeneously integrated photodetector has a strong optical response at 0 V bias, which is attributed to a strong built-in field. From the grating coupler efficiency and fiber-referred responsivity measurement, the photodetectors have a waveguide referred responsivity higher than 0.5 A/W over the 2.2-2.42 μ m wavelength range. The measured peak internal efficiency is 1.2 A/W at 2.32 μ m wavelength.



Figure 6. (a) The coupling efficiency of the grating coupler, (b) dependence of the fiber coupled responsivity on the reverse bias at wavelength of 2.3 μ m and 2.4 μ m, (c) responsivity as function of the input laser wavelength under reverse bias of 0 V and 0.5 V.

CONCLUSION

High performance SWIR AWGs and hybrid InGaAs/GaAsSb type-II quantum well photodetectors are fabricated and characterized. The low resolution (5 nm) and high resolution (1.4 nm) SWIR AWGs have a crosstalk of -30 dB and -25 dB, and insertion loss of 2.5 dB and 2.8 dB, respectively. The photodetectors have a low dark current of 12 nA and a high internal responsivity of 1.2 A/W at a wavelength of 2.32 μ m. The epitaxial stack for this photodetector integration can also be used to realize SWIR hybrid lasers, therefore enabling a fully integrated spectroscopic system.

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