A novel approach to create a tunable Fano resonance with an extinction ratio over 40 dB

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Abstract—We experimentally demonstrate a novel method to make a tunable Fano resonance. Based on a silicon microring with two tunable reflectors inside, we are able to generate a tunable Fano resonance with maximum extinction ratio over 40 dB and a slope rate over 700dB/nm.

Index Terms—Fano resonance, Ring resonator, Integrated reflector, Silicon photonics.

I. INTRODUCTION

Resulting from the interference between a continuum background mode and a discrete resonance mode[1], a Fano resonance can exhibit an extremely sharp slope, which can benefit many applications such as optical switches, optical sensors, laser cavities and electro-optical modulators. Only a tiny wavelength shift is needed to achieve a large power variation, which usually translates into high efficiency, low power consumption and high speed. Silicon photonics, on the other hand, is one of the most promising platforms to implement those applications due to its ultra high index contrast and CMOS compatibility, which allows very compact device footprint and large-scale, low-cost manufacture.

Various approaches have been reported to demonstrate a Fano resonance in silicon photonics, but a satisfying solution towards a fully integrated circuit with an efficient tuning mechanism to generate a fully tunable Fano resonance with adequate extinction ratio (ER) and slope rate is still missing[2], [3]. In this paper we propose a novel method based on an integrated photonics circuit on silicon-on-insulator platform. With simple thermo-optic heaters, we can achieve efficient tuning of the generated Fano resonance with a maximum extinction ratio (ER) over 40 dB and a corresponding slope rate more than 400 dB/nm. In terms of the maximum slope rate, a value over 700 dB/nm is observed at some resonances with an extinction ratio around 36 dB. The maximum observed ER and slope rate, to our best knowledge, are the largest realized so far in an integrated circuit.

II. SIMULATION

The schematic of our device is given in Fig. 1. It can be described as a silicon ring resonator with two integrated tunable reflectors inside (indicated in red). These two reflectors will form a Fabry-Perot (FP) cavity embedded in the ring resonator. As a consequence, the mode from FP cavity will interfere with the resonance mode of the ring resonator at its *drop* port. When the FP mode is in slowly varying condition

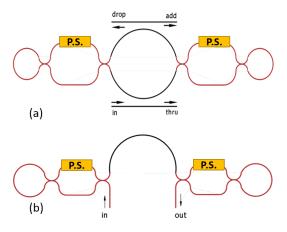


Fig. 1. Schematics of our proposed device (a) and the Fabry-Perot cavity formed by the reflectors (b)

(low Q factor and large free spectral range), the interference of these two modes will generate Fano resonance. This condition can be achieved by controlling the reflectivity of the two reflectors, as the FP mode depends strongly on the reflectivity. As for the reflector, which is a loop-ended Mach-Zenhder-Interferometer, its reflectivity can be tuned from 0 to almost 100% by adding $\frac{\pi}{2}$ phase shift on one arm of the MZI[4].

We simulate our device using the optical circuit simulator-Caphe by Luceda Photonics. In Fig. 2(a) and (c), the spectra of the embedded FP cavity formed by the two tunable reflectors are shown. Clearly, by adding different phase shifts on one arm of the reflector, the FP output can be tuned between very sharp mode and slowly varying mode. Note that for simplicity, we only tune one reflector, while in practice both reflectors can be tuned independently. In Fig. 2(b) and (d) we plot the corresponding output of the complete device. Due to the intentional reflections inside the ring, the output of the device shows strong resonance splitting. The difference in the shift rates of the two peaks within one split resonance is consistent with former observation and has already been well explained[4]. Fano resonances are observed in multiple conditions. Generally speaking, there is a tendency that slowly varying FP mode will lead to a Fano resonance with larger extinction ratio.

The devices were fabricated through Europractice Multi-Project-Wafer (MPW) service on 200mm CMOS line of