

Porous multi-junction thin-film silicon solar cells for scalable solar water splitting



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ABSTRACT

Monolithic solar water splitting devices implemented in an integrated design approach, i.e. submerged in the electrolyte, pose a significant limitation when it comes to up-scaling. The ion transport distances around the monolith are long and consequently, the ionic Ohmic losses become high. This fact turns out to be a bottleneck for reaching high device efficiency and maintaining optimum performance upon up-scaling. In this paper, we propose a new device design for integrated monolithic solar water splitting based on porous multi-junction silicon solar cells. Simulation results highlight that porous monoliths can benefit from lower ionic Ohmic losses compared to dense monoliths for various pore geometries and monolith thicknesses. In particular, we show how micrometer scale pore dimensions could greatly reduce Ohmic losses, thereby minimizing overpotentials. A square array of holes with a diameter of 20 μm and a period of 100 μm was fabricated on single-junction and multi-junction amorphous and microcrystalline silicon solar cells. A small impact on the open circuit voltage (V_{oc}) and short circuit current density (J_{sc}) was obtained, with porous triple junction solar cells reaching V_{oc} values up to 1.98 V. A novel device design is proposed based on porous triple-junction silicon-based solar cells.

1. Introduction

Electrification offers a great potential to enable the de-carbonisation of energy [1]. According to several studies, a sustainable energy system based on 100% renewable energy sources, of which the majority is wind and solar, should be possible [2,3]. However, the intermittent nature of wind and solar is a bottleneck for large-scale grid implementation. In order to balance generation and demand, the storage of produced energy is imperative. The storage of renewable electricity in the form of chemical bonds, such as hydrogen (H_2), could fill in the gap and empower a sustainable energy system [4]. Other than storage, green hydrogen could play an important role in such a renewable energy system for the de-carbonisation of transportation, building heating and as fuel for industrial use.

Among the many different techniques which can achieve the conversion of water and sunlight into hydrogen (e.g. PV-electrolysis, thermal conversion and molecular artificial photosynthesis), photoelectrochemical (PEC) water splitting is a promising approach [5–8]. Apart from the properties and performance of the involved materials in

a PEC device (photoabsorbers with optimal band gaps, catalysts, protective coatings, etc.) the cell design is a key element not only for enabling high solar to hydrogen efficiency (STHE, i.e. the ratio of the available chemical energy in the generated hydrogen to the incident solar energy), but also for maintaining those high values when up-scaling for large-scale implementation.

The majority of lab scale PEC demonstrators employ the wired configuration, shown in Fig. 1a. With this design, Khaselev et al. have demonstrated a device reaching an STHE of 16.4% [9]. However, their approach includes expensive III-V materials which are too costly for terrestrial applications. In a cost-effective approach based on triple junction silicon solar cells, the same group showed a device reaching 7.8% [9], while more recently Urbain et al. reported a device reaching an STHE of 6.8% [10]. In order to reduce cost, the wireless monolithic design (Fig. 1b) has been proposed. With this design, Reece et al. reported a 2.5% efficient PEC device [11]. They attributed the lower STHE value compared to their wired reference device, which reached an STHE of 4.7%, to the high Ohmic losses due to long ion transport distances around the monolith. In fact, the ionic Ohmic losses are

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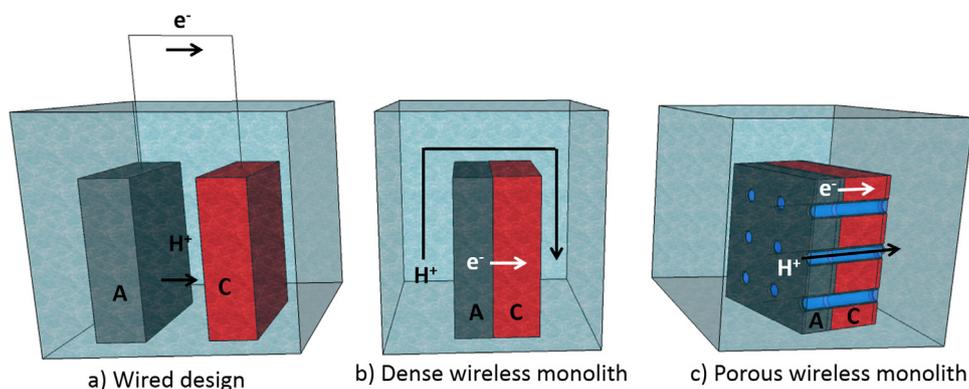


Fig. 1. Photoelectrochemical cell configurations for monolithic integrated solar water splitting: (Photo)anode (A) and (Photo)cathode (C) in a) a wired configuration where the ionic transport distance is defined by the distance between the two photoelectrodes, usually in the cm range, b) the monolithic wireless approach where the ionic transport distance is defined by the thickness and surface area of the monolith and c) porous monolithic wireless configuration where through plane pores act as ionic shortcuts and the ionic transport distance can reach a few tens of μm .

higher in a wireless than a wired design (electronic conductivities are much higher than the ionic conductivities), thus limiting the efficiencies [12]. However, this approach offers an alternative to satisfy the efficiency/cost trade-off [4]. Towards this direction of integrated monolithic devices, the idea of porosity (Fig. 1c) for solar water splitting has been mentioned conceptually [7,12], has been demonstrated in simplified monoliths [13], while theoretical works have touched upon the concept of ionic shortcuts [14,15]. In particular, as discussed by Haussener et al. [14], for monolithic PEC configurations, small electrode lengths and thin separation distances lead to reduced Ohmic losses because of short ion transport distances. For an operation sustained by water vapor, a monolith width of less than $300\ \mu\text{m}$ is proposed for reducing the overpotential to values lower than $100\ \text{mV}$, even for a thickness of $1\ \mu\text{m}$ [15]. According to such design guidelines, through plane pores incorporated on the monolith could act as ionic shortcuts, minimizing Ohmic losses. To further investigate this idea, simplified perforated monoliths were fabricated experimentally, and their Ohmic losses were evaluated. As shown by Bosserez et al. [13], the Ohmic losses were kept at acceptable levels (below $100\ \text{mV}$ of overpotential) provided a pore spacing less than $1\ \text{mm}$ and electrolytes at extreme pH values.

In this paper, we propose a novel device design based on porous multi-junction thin-film silicon solar cells. In such a design, the multi-junction solar cells could provide the required high voltage values for the water splitting operation while micron-scale porosity would act as a shortcut for ions, minimizing their transport distances and thus, the respective Ohmic losses. Therefore, the achieved STHE values could be maintained upon up-scaling for cost-reduction. Our intention is to provide a new design approach, based on the concept of micron-scale porosity, so as to trigger discussions and activities within the scientific community. Such a paradigm shift in the device design of monolithic PEC cells could hopefully help to further develop new designs deviating from more conventional concepts.

In the following sections, we will present our simulation results on porous monoliths so as to highlight the potential of this approach. Furthermore, we will present the experimental fabrication of micron-scale pores on single-junction and multi-junction thin-film silicon solar cells and we will discuss the impact of introducing porosity on their performance. Finally, we will discuss the perspectives of this approach and present our envisioned device design.

2. Material and methods

In order to investigate the potential of the concept of porous PEC monoliths, 2D electrochemical (EC) simulations were performed using the Tertiary Current Distribution electrochemistry module of COMSOL Multiphysics [16]. In our simulations, the system was considered to be

periodic in the horizontal direction with monoliths of various periodicity (P), pore width (w) and thickness (t) operating at electrolytes with different pH , as shown in Fig. 2. The reaction kinetics at the anode and cathode sides were considered to follow the Butler-Volmer equation. The pore sidewalls were considered to be perfectly insulated and do not act as reaction sites. In order to focus only on the effect of the geometrical parameters on the monolith's performance, we restricted the simulations on state-of-the-art high performing reaction kinetics considering Pt electrode parameters for the cathode and RuO_2 parameters for the anode.

The current flows in the EC system through ion transport in the electrolyte were modelled with $J_i = F \sum_{i=1}^n z_i (-D_i \nabla c_i - z_i \mu_{m,i} F c_i \nabla \phi_i)$, where J_i is the electrolyte current, F is the Faradaic constant z_i is the valence charge of the considered ionic species, D_i is the diffusion coefficient, c_i is the concentration of ionic species, $\mu_{m,i}$ is the effective mobility of the ionic species, ϕ_i is the electrolyte potential, and the i sub-index refers to contribution from a specific ionic species. We assumed that there is no electrolyte flow and thus the current transport is only due to ionic charge's electric field driven transport (migration) and diffusion. We considered sulphuric acid (H_2SO_4) as the electrolyte at various pH , with only H^+ and HSO_4^- ions in the electrolyte. Transport of ionic species through the ion exchange membrane were assumed to follow the same equation but for Nafion's corresponding diffusion coefficients. The ionic species concentration in the electrolyte follows the electroneutrality condition $\sum_{i=1}^n z_i c_i = 0$. In our calculations, the electrodes were considered to be perfectly conducting, the gas species were considered to be immediately extracted upon generation at the electrodes and crossover effects were ignored. We always ensured that the top and bottom computational domain boundaries were sufficiently far away such that there was zero current at these boundaries. The EC current was calculated by integrating the reaction current density at the anode or cathode active surface and normalizing it to the total surface, which includes the membrane area where no water splitting reaction occurs. All relevant physical constants were taken from the contribution of Haussener et al. [14].

The thin-film silicon solar cells were deposited on glass carriers in a medium-sized ($40 \times 50\ \text{cm}^2$) single-chamber plus load lock R&D reactor by plasma enhanced chemical vapor deposition (PECVD). All solar cells were made in a p-i-n configuration, meaning that the respective layers (p, i and n) were deposited in that order. The single and tandem solar cells were used in a superstrate configuration while the triple junction solar cells were used in the substrate configuration. The absorber layers consisting of hydrogenated amorphous silicon (a-Si:H) have a thickness of $500\ \text{nm}$ for the single junction cells while for the tandem and triple junction cells the a-Si:H thickness is $130\ \text{nm}$ and $500\ \text{nm}$ for the top and second layers, respectively. The microcrystalline silicon cells ($\mu\text{-Si:H}$) have an absorber layer thickness in excess of

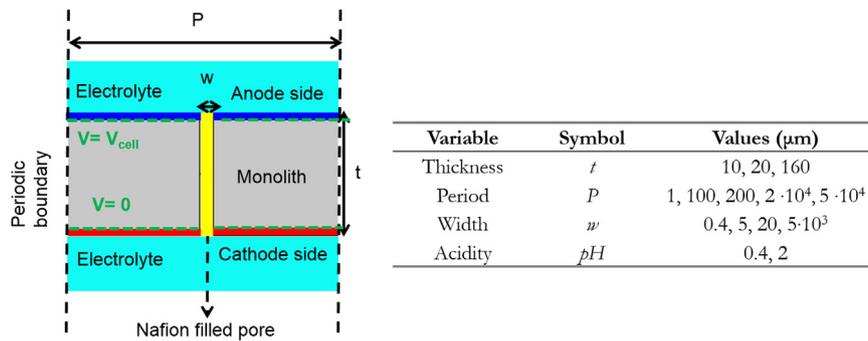


Fig. 2. 2D simulated photoelectrode-EC system (left) and values of the dimensions of the investigated design monoliths (right).

1 μm . All layers were deposited from a gas mixture including SiH_4 and H_2 . For the p-type layers, trimethylborane ($\text{TMB} = \text{B}(\text{CH}_3)_3$) was used as the dopant gas, whereas for the n-type layers phosphine (PH_3) was used. The 2 μm thick transparent conductive contacts consist of boron doped zinc oxide (ZnO:B), and were fabricated by low pressure chemical vapor deposition (LPCVD) in a home-made system from a gas mixture of diethyl-zinc ($\text{Zn}(\text{C}_2\text{H}_5)_2$), H_2O and diborane (B_2H_6).

A square array of cylindrical holes with a period of 100 μm and a pore diameter of 20 μm was fabricated by optical lithography and dry etching. For the silicon part of the cells, reactive ion etching (RIE) was used while for the ZnO:B layers inductively coupled plasma (ICP) etching was preferred over wet etching (HF or NH_4Cl) due to minimal under-etch and therefore less impact on the film's sheet resistance. The porous and non-porous solar cells were characterized by current density – voltage (J-V) measurements at 25 $^\circ\text{C}$ under AM1.5 filtered 1-sun

illumination using a single beam Xenon lamp solar simulator from Oriol. For the porous solar cells that the photoactive material has been etched, the current density has been normalised to take into account the removed area.

3. Results

3.1. Effect of porosity on electrochemical performance (simulation results)

The goal is to check the influence of geometry on the EC behaviour of the system and compare porous and dense monoliths with respect to Ohmic losses. The J-V characteristics for various porous monolithic geometries are shown in Fig. 3. We first depict the influence of monolith thickness and pore width for a constant electrolyte pH of 2 (Fig. 3a). Three different thicknesses were considered: 10, 20 and

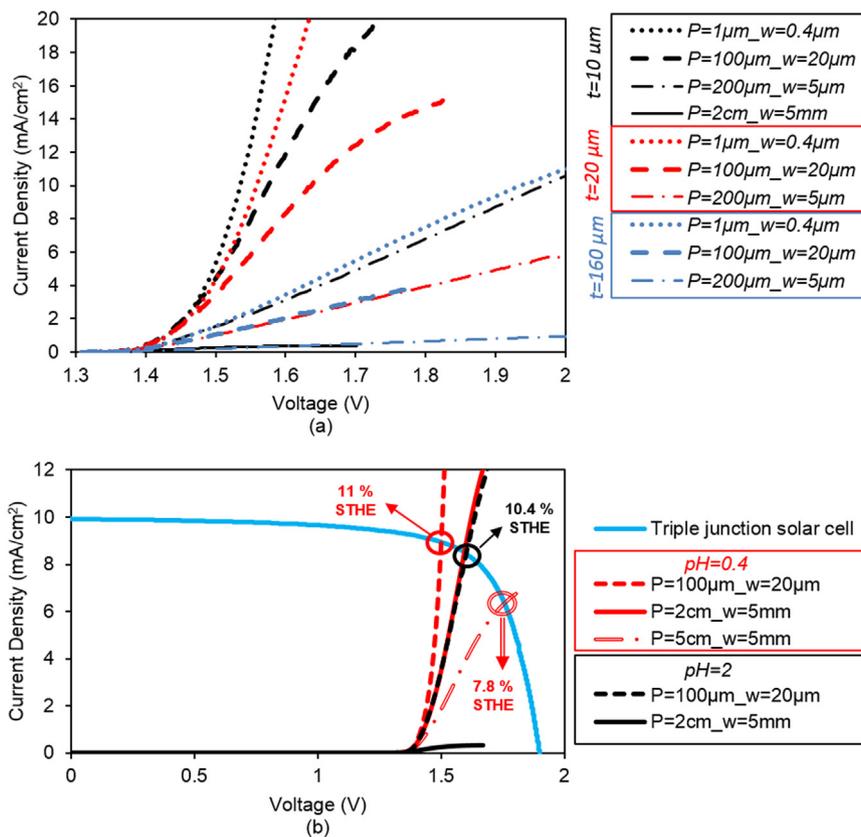


Fig. 3. Electrochemical J-V curves simulated with COMSOL Multiphysics for (a) various pore geometries and monolith thickness at an electrolyte pH of 2 and (b) selected porous and dense monolith geometries at pH of 0.4 and 2 superimposed with the J-V curve of the best reported multi-junction silicon solar cell [24]. (For interpretation of the references to color in this figure, the reader is referred to the web version of this article.)

160 μm . The 10 μm thickness corresponds to a relevant thickness of the full material stack (silicon based photoabsorbers and transparent conductive oxides) of a multi-junction thin-film solar cell. This case represents the design where the solar cell is located on top of a highly porous and catalytically active substrate and therefore the effective distance for the ions is that of the solar cell. The 160 μm case is that of a low resistivity silicon wafer (~ 150 μm thick) on top of which the thin-film multijunction solar cell (~ 10 μm thick) is deposited. Finally, the 20 μm thickness corresponds to the case of the thin-film multijunction solar cell (~ 10 μm) on top of an ultrathin (~ 10 μm) single-crystal silicon foil [17], a device with a thickness that could be processed using standard equipment, as already demonstrated for solar cell devices [17,18].

We can first focus on the 10 μm thickness case (black curves of Fig. 3a) and the performance of three different pore geometrical cases. These pore geometrical cases represent different regimes and their performance is benchmarked with respect to dense monoliths (2 cm and 5 cm electrode width separated by 5 mm ion exchange membrane) which is used as a reference. The dotted J-V curve represents the EC characteristics of a porous monolith where the ion transport pore geometry and distribution is in the size regime comparable to the wavelength of light, thus enabling photonic light trapping. In particular, we considered a period of 1 μm and a pore width of 400 nm. With such period and width, the pores essentially function as a diffraction grating or photonic crystal system, which may also enhance light absorption based through oblique angle scattering that increases the optical path length of light in the solar cell [19,20]. Solar cell devices employing a photonic assisted light trapping scheme have been already proposed and realized in thin solar cells [21]. However, it should be mentioned that the chosen period and pore width are not optically optimal but are rather used here as a first demonstration. For further fine tuning optical simulations are required. The dashed-dotted J-V curve represents the case where the size scale is much larger with a period of 200 μm and a pore width of 5 μm . This configuration corresponds to a design where 200 μm wide photoelectrode “islands” are separated by 5 μm gaps similar to a micro-fluidic approach [22]. In such system, the proton ions formed at the centre of the “island” would have to travel a significantly longer distance and thus suffer from more effective electrolyte resistance. As a result, the J-V curves of the two cases have significant differences, with the latter one requiring a higher voltage for an operation at a specific current density. For example, for an operation at 10 mA/cm^2 , the first small scale photonic approach requires an overpotential of ~ 300 mV to drive the water splitting reactions while the large scale “islands” requires an overpotential as high as 770 mV. Finally, an intermediate case is represented by the dashed J-V curve with a period of 100 μm and a pore width of 20 μm . This configuration presents a scenario, where conventional patterning techniques such as UV lithography, could be used for its realization. High current densities can be achieved for a small overpotential deficit compared to the much stricter photonic case. All porous configurations mentioned above have EC J-V characteristics which clearly outperform the non-porous reference case (solid J-V curve). For the non-porous monolith, the ionic transport distances are so large that mass transport limitations of the ions dominate the J-V characteristic, which result in a flattening of the J-V characteristic beyond 1.5 V of applied voltage (black solid line curve, Fig. 3a and b). As a result, the current densities are restricted to values lower than 1 mA/cm^2 . For a pore period of 100 μm , width of 20 μm and pH of 2 (red dash curve Fig. 3a), one can also see the onset of the mass transport limitations at ~ 1.75 V though a sufficiently large current density in the electrolyte is already obtained.

Results for two more thicknesses (i.e. 160 μm and 20 μm) are shown in Fig. 3a with red and blue colour, respectively. The same trend as for the 10 μm case is seen for the different pore geometries with the extra overpotential penalty which has to be paid for the additional thickness through which ions have to travel (curves of similar colour but different dash type should be compared here). It should be mentioned that the J-

V curves of the dense monoliths for those two thicknesses were lower than the 10 μm case and they are omitted from the figure for better clarity. Overall, the thinner the monolith is, the lower the Ohmic losses get (curves of different colour but similar dash type should be compared here). However, practical limitations such as the ability to process thin substrates should be taken into account for defining the most relevant thickness.

The electrolyte pH case of 0.4 was also investigated. As shown in Fig. 3b, porous and dense monoliths operating in an electrolyte with a pH of 0.4 can achieve better J-V performance with lower Ohmic losses compared to an electrolyte with a pH of 2 (red vs black curves). However, we should note here that for similar changes in the pH of the electrolyte, the device stability has been shown to be significantly affected [23]. Therefore, by incorporating pores, we could relax the requirements for operation in electrolytes at extreme pH which corrode the photoelectrodes compromising the device lifetime and stability. This can be seen by the similar J-V behaviour which was obtained for the dense monolith with an electrode of 2 cm at pH 0.4 (solid red) and the porous monolith at pH 2 (dashed black). Nevertheless, when it comes to upscaling, a dense monolith's Ohmic losses will further increase when its size increases thus limiting the STHE values that could be achieved. This is clearly highlighted when comparing the dense monoliths with electrode dimensions of 2 and 5 cm. On the contrary, since porosity actually offers no restrictions in up-scaling, the achieved STHE values for porous monoliths could be maintained when up-scaling the device.

In order to show the potential of the porosity concept with respect to the STHE values that could be achieved, we consider the best reported triple junction thin-film silicon solar cell [24]. We can pinpoint the operating point by superimposing the J-V curve of the triple junction PV [25] with the simulated EC J-V curves for the case of a monolith of 20 μm in thickness. The operating point would then be at the intersection of the two J-V curves (Fig. 3b). For this discussion, no device degradation due to the incorporation of the pores is considered while a Faradaic efficiency of 100% is assumed. As highlighted in Fig. 3b, an STHE of 11% should be possible (red circle highlighting the operating point) for a porous monolith at $pH = 0.4$. The STHE decreases slightly to 10.4% (black circle highlighting the operating point) when a higher pH electrolyte value is considered. For the dense monoliths, the Ohmic losses at $pH = 0.4$ increase when increasing the monolith size from 2 to 5 cm resulting in a decrease in STHE from 10.4% to 7.8%. While the STHE will further decrease for bigger dense monoliths even at extreme pH , porous monoliths could be upscaled without limitations in size. Therefore, a porous monolith even in relaxed pH conditions offers the potential for better STHE without limitation to upscaling.

3.2. Impact of porosity on the PV performance (experimental results)

On the device level, etching a solar cell to introduce corrugations can be expected to have a strong impact on the quality of the photoactive material [26], affecting the cell's overall performance, thereby compromising the achievable STHE. Therefore, the most appropriate monolithic PV technology is the multi-junction thin-film silicon solar cell with amorphous silicon (a-Si) and/or microcrystalline silicon ($\mu\text{-Si}$) as the photoactive layers. On one hand, such multi-junction solar cells can provide high voltage. On the other hand, the involved photoactive materials are less vulnerable to damage compared to their crystalline silicon counterparts (both having intrinsically short diffusion lengths) and can be easily optimised in order to ensure strong absorption and optimal usage of the solar spectral range.

In order to investigate to what extent porosity affects the solar cell operation we focused on a-Si, $\mu\text{-Si}$ single-junction as well as multi-junction (tandem and triple junction) solar cells. Each step of the pore fabrication process has an impact on the device performance and its respective parameters, i.e. short circuit current (J_{sc}), open circuit voltage (V_{oc}), fill factor (FF) and efficiency (η). To highlight this, we show

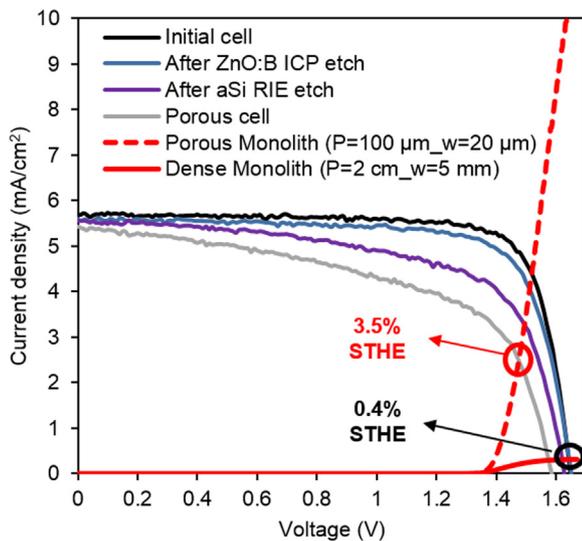


Fig. 4. Impact of porosity on the solar cell performance: illuminated current density – voltage scans for a-Si based tandem solar cells without pores (black curve), after etching the rear ZnO:B layer (blue), after etching the a-Si layers (purple) and with pores through the whole cell stack (gray). The EC J-V curves for the dense (2 cm electrode width separated by 5 mm ion exchange membrane) and porous (period of 100 μm and pore width of 20 μm) monoliths are included in order to highlight the respective achievable STHE. (For interpretation of the references to color in this figure legend, the reader is referred to the web version of this article.)

in Fig. 4 the illuminated J-V scans for the case of an a-Si tandem solar cell (500 nm bottom a-Si/130 nm top a-Si).

The first ZnO:B ICP etch, which is actually taking place on the rear of the solar cell, has an impact mainly on the collection of photo-generated carriers. In particular, because of an increase in the sheet resistance of the ZnO:B film, the FF decreases. The subsequent step of RIE on a-Si causes a slight decrease in all parameters. By etching the photoactive material, extra recombination sites (unpassivated dangling bonds that extend inside the pores as a result of the etching process and the respective surface area enhancement) are introduced, thus affecting the achievable V_{oc} values. However, the V_{oc} was only slightly affected despite the fact that we etched through the photoactive material. Finally, for the porous solar cell, a further decrease in all cell parameters is observed with a decrease in V_{oc} as low as 63 mV.

The same behaviour was observed for the rest of the studied solar cell devices. Plotting the values of each solar cell parameter before and after introducing porosity, we can notice some trends shown in Fig. 5. The impact of porosity is more pronounced on the $\mu\text{c-Si}$ than on the a-Si solar cells. On one hand, while 2.5 mA/cm^2 were lost when introducing porosity on the $\mu\text{c-Si}$ solar cells, the loss was restricted to below 1 mA/cm^2 for the porous a-Si solar cells. On the other hand, the defects introduced on the photoactive material by the implementation of porosity resulted in a higher decrease in V_{oc} for $\mu\text{c-Si}$ solar cells (40 mV) compared to the a-Si cells (16 mV). The shorter diffusion lengths for the a-Si solar cells compared to the $\mu\text{c-Si}$ solar cells could be responsible for such a different impact on V_{oc} as fewer generated carriers reach the pore sidewalls which act as recombination sites. The increased amount of recombination centres, apart from the aforementioned impact on V_{oc} , similarly leads to a decrease in FF. Finally, while the impact of porosity on the efficiency of $\mu\text{c-Si}$ solar cells was high (relative difference of 29%), a-Si solar cells suffer only from a limited impact (relative difference of 14%).

For the multi-junction solar cells, two cases were considered: a tandem (500 nm a-Si bottom/ 130 nm a-Si top) cell and a triple (1 μm $\mu\text{c-Si}$ bottom/500 nm a-Si middle/130 nm a-Si top) cell. The decrease in performance was smaller for the a-Si tandem cell compared to the triple

junction cell. Nonetheless, the triple junction solar cell had only a marginal decrease in V_{oc} of 162 mV (8% relative difference, from an initial value of 2145 mV to 1983 mV after the implementation of the pores), highlighting the minimal material degradation when implementing pores in the device. Similar to the V_{oc} , the J_{sc} value of the porous triple junction solar cell was only 7% lower compared to the non-porous cell. It should be mentioned here that the low J_{sc} values obtained for the triple junction solar cells is attributed to a non-optimal current matching because of a thinner $\mu\text{c-Si}$ layer. For both tandem and triple junction cells, the FF was the most affected parameter. The different impact on the FF among the single junction and multijunction solar cells could be explained by the fact that the a-Si top cell in the multi-junction cells is thinner (~ 130 nm) than the respective a-Si single junction cell (500 nm). Although for both single-junction and multi-junction cells the electronic defects at the edges of the pores might provide a conductive path between both contacts, thereby partially shunting them, the top cell in the multi-junction cells is very thin thus making the effect more pronounced. In fact, the multi-junction cells had a decreased shunt resistance compared to the single-junction cells. Nevertheless, such a high V_{oc} (1983 mV) achieved for the porous triple-junction solar cells offers some degrees of freedom for designing a solar water splitting porous device. In particular, since any potential which is higher than the operating potential is actually not contributing to the increase of the STHE, a device with lower V_{oc} could be afforded. Considering the aforementioned results, a triple-junction solar cell could be specifically designed for the application of porous solar water splitting [23,27], targeting to achieve a better trade-off between photovoltage – photocurrent and to mitigate the degradation in FF.

Finally, despite the device degradation from the incorporation of holes, the benefit of our approach can be easily highlighted by focusing on the expected STHE values, i.e. the operating point resulting from the superposition of the simulated EC J-V results with the experimental J-V PV curves (Fig. 4). In particular, the impact on the achievable STHE due to the loss in solar cell device performance (red circle as the operating point of the porous monolith in Fig. 4) is smaller than the impact on the achievable STHE due to the Ohmic losses from the long ion transport distance around the dense monolith (black circle as the operating point of the non-porous monolith in Fig. 4). It should be noted that the absolute numbers of the STHE discussed here are only indicative for the specific case of the tandem a-Si based solar cell. The reason that the STHE values mentioned in Fig. 4 are lower compared to the STHE values mentioned in Fig. 3b is related to the difference in the IV of the involved solar cells. Compared to the tandem a-Si solar cell shown in Fig. 4, the triple junction cell such as the one in Fig. 3b results in a higher V_{oc} value and a better current matching leads to a higher J_{sc} value thus resulting in higher STHE values. In general, differences between theoretical limiting efficiencies and the STHE values of practical devices are due to the properties of the involved materials as well as their optimal combination, which defines the performance of the initial device [28], while for porous devices the degree of device degradation after the incorporation of pores would further restrict the final STHE values. Starting from a highly performing multi-junction silicon solar cell such as the record efficiency cell reported by Sai et al. [24], an STHE value of around 7–8% could be achieved assuming a similar degradation as the one reported here. While such STHE is comparable to state of the art thin-film silicon based monolithic devices at a miniature scale (device demonstrators are usually less than a cm in size), the approach of porous multijunction solar cells offers the advantage of upscaling the device without any limitation in size. Possible ways to decrease the difference between theoretical and practical devices include i) designing of a solar cell for an operation in a solar water splitting device with a relevant current-voltage trade-off [28] and ii) the restoration of the solar cell degradation by treatments, such as hydrogenation, employed after the pore fabrication process.

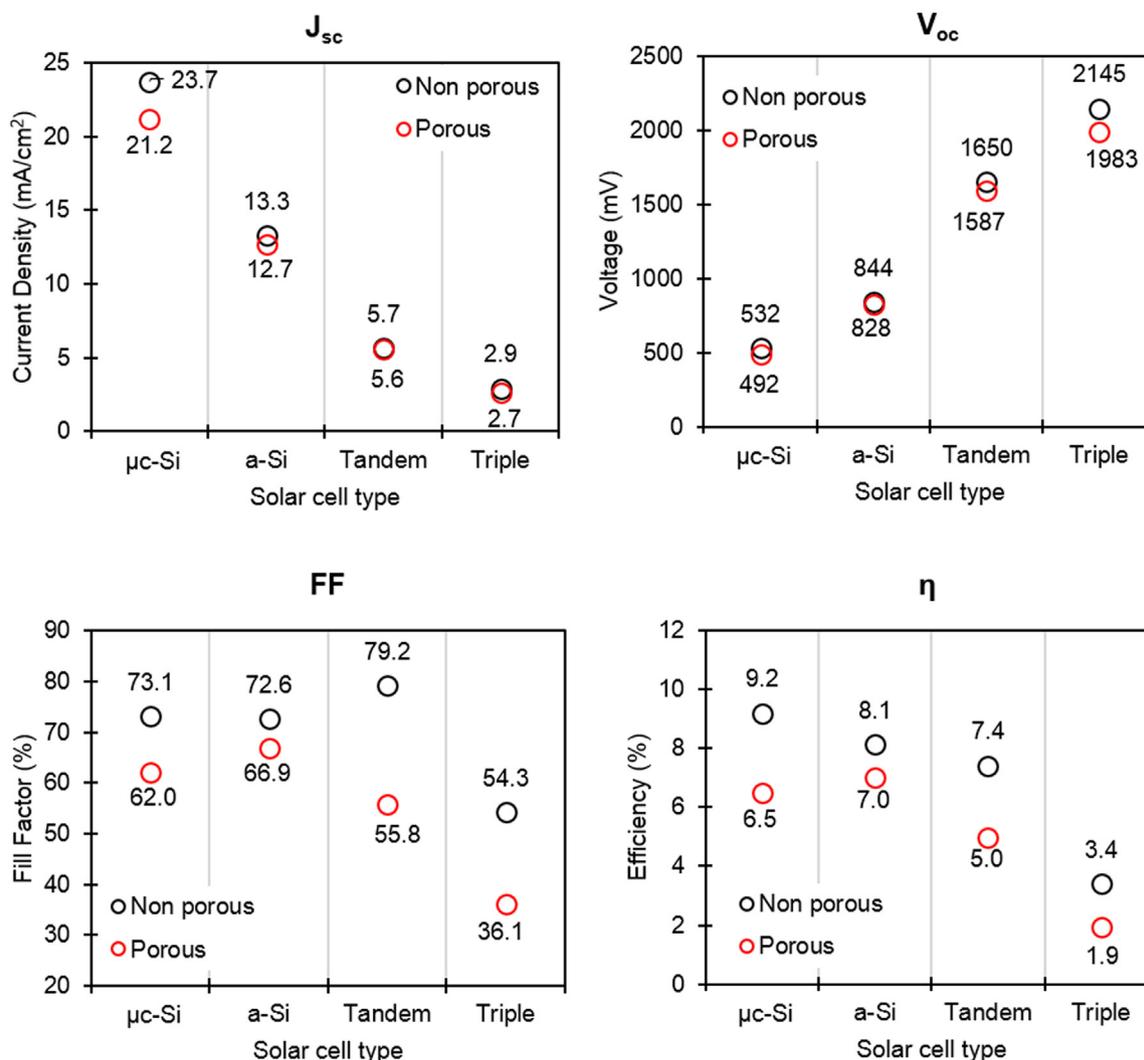


Fig. 5. Solar cell parameter values for non porous and porous cells and for various single-junction and multi-junction solar cell types.

4. Discussion and perspectives

Although the PEC approach is conceptually simple and elegant, it has proven challenging to reach practical implementation since it combines complex mechanisms involving light-matter interaction, semiconductor physics and electrocatalysis. It is for this reason that ever since the highest efficiency device demonstration [9], endeavours to reach a similar efficiency with a stable material system have had limited progress, even if a lot of research has been dedicated to improving the involved processes. To date, not only is there no system that surpasses the 16.4% efficiency mark with a low cost approach as well as long and stable device operation, but also reported devices are still far from the theoretical upper limits in efficiency [28]. Moreover, scalability is too often overlooked in existing device architectures.

The proximity of PEC solar water splitting to the much more mature field of PV could be an advantage. The learning curve of PV has driven the whole field towards a more coordinated activity. In particular, internationally accepted strategies were introduced, such as standard characterization methods and reporting certified efficiencies, as well as the adaptation of the device designs according to economy-related concepts such as the levelised cost of electricity. As a result, the highest PV efficiencies achieved [29], are closer to the theoretical upper limits than is the case in the solar water splitting field [28]. During this process of PV maturation, certain technologies had to fade out while the learning curve still continues through process simplifications for cost

reduction [30], as well as optimisation based on the passage from the cell to the system level [31]. Similarly, in an effort to better coordinate the focus and the employed strategies for the solar water splitting field, instructions for benchmarking the methodologies and characterization procedure of solar fuel cells have been published [32]. Moreover, the use of consistent terminologies has been proposed [33], and an overview of the highest efficiency devices so far has been reported [8,34]. The learning curve of a mature field such as PV could be taken into consideration so as to target design concepts that have the highest potential to result in a technology breakthrough.

Device lifetime, efficiency and scalability (up-scaling) are three aspects which, if properly optimised, could lead to a technology breakthrough for the solar water splitting field. However, there is a clear compromise between the aforementioned figures and cost. As discussed by Dumortier et al. [35], the choice of device design requires a holistic approach with a simultaneous fulfilment of the aforementioned elements. In terms of device lifetime, most of the photoelectrodes in a PEC configuration suffer from degradation due to the chemical reactions which are taking place during the device operation. As highlighted by Ager et al. [34], the majority of device demonstrators so far have a very restricted stability (< 1 day). For that reason, solid state junctions are preferred over semiconductor/liquid junctions for long-term durable operation. Moreover, surface coatings have been developed for material protection during operation in extreme pH values [36]. However, both solid state junctions and surface coatings increase the cost of the

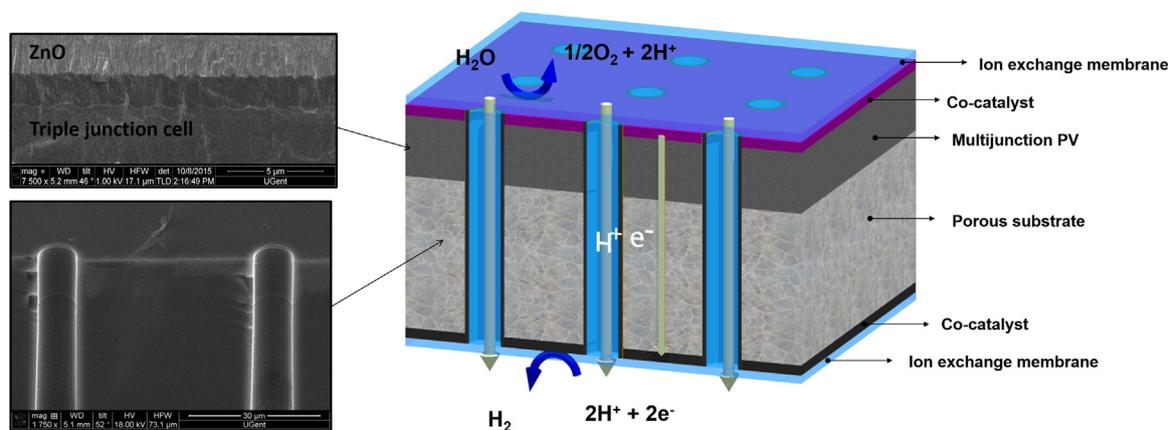


Fig. 6. Proposed porous monolithic photoelectrochemical device: triple-junction silicon-based solar cell (top left) deposited on highly-doped silicon foil and porosity introduced by means of optical lithography and deep reactive ion etching (bottom left).

manufacturing process and should be taken into account during device design. Nonetheless, surface coatings have so far not resulted in device lifetimes comparable to PV (20–25 years), while slight changes in the electrolyte pH (from 13 to 14) result in significant stability degradation even when solid state junctions are used [23]. Regarding efficiency, there is a clear compromise between expensive III-V and cost-effective silicon based materials [9]. However, from an analysis of various device configurations [35], it was shown that hydrogen could be produced at affordable prices in low cost systems with efficiencies around 6%. Finally, up-scaling is one element that defines the potential for large-scale implementation of a certain technology as it strongly affects cost. So far, PEC device demonstrators are small scale while hydrogen and oxygen co-evolve in the same compartment without involving the use of membranes. For the specific case of monolithic integrated PEC devices, up-scaling is heavily obstructed due to high Ohmic losses in the electrolyte, as discussed earlier.

Following an interdisciplinary approach, taking into account the aforementioned aspects, we describe here an innovative approach for a porous monolithic PEC device. In Fig. 6, a schematic of the envisioned device is shown. Efficient and stable operation in a cost-effective fabrication can be provided by triple junction solar cells. Their performance parameters can be tuned in order to match the specific application of porous solar water splitting taking into account the device degradation during pore fabrication. In particular, a-Si cells are preferred compared to μ c-Si because of the minimal damage after the incorporation of the holes. Since the involved solar cells are based on the thin-film PV technology, their thickness is not sufficient for mechanical stability and therefore, one of the prerequisites of the envisioned device is the need of a conducting, porous but mechanically stable substrate. For that purpose, a highly conductive thin silicon foil could be used. Epitaxial foils have already been used for thin crystalline silicon solar cells [37]. The micron-scale porosity could be incorporated by means of low cost lithography in roll to roll configurations and deep reactive ion etching [38]. Additionally, by minimizing the overpotential related to the Ohmic losses in the electrolyte via the incorporation of holes/shortcuts, an overpotential deficit could be afforded elsewhere in the device. More precisely, contrary to the state-of-the-art high efficiency devices which require expensive platinum group metals, earth-abundant catalysts such as NiMo and NiFe could be used for further cost reduction. Highly transparent catalysts and an ion exchange membrane should be employed at the front side. The ion exchange membrane should be thin enough to optimise gas transport and avoid parasitic absorption of light (absorption of light in materials which do not contribute to the photogenerated current and thus limit the device absorption and current generation) while it should be thick enough to optimise ion transport and diffusion limitations. At the rear side, a stack of relevant materials can be optimised for providing optical reflection of

the incident light back into the photoactive materials and promoting the catalytic reaction. Overall, the conceptual device based on porous triple-junction silicon solar cells could offer a paradigm shift towards efficient, long-lasting and scalable devices targeting cost-effective implementation.

5. Conclusion

In this paper, we presented a novel design for scalable monolithic integrated solar water splitting. In particular, we discussed the concept of porous multi-junction solar cells. Silicon based multi-junction thin-film solar cells could provide high STHE values in a low-cost fabrication and long durable operation while porosity would act as shortcuts for ions so as to maintain high STHE when up-scaled. On one hand, we have tried to investigate to what extent porosity has an impact on reducing overpotentials related to high Ohmic losses. Simulations show that dimensions on the order of a few tens to a few hundreds of μm could enable low ionic Ohmic losses. With such micro-scale pores, the electrochemical simulation indicate that one could potentially reach an efficiency of 11% at $pH = 0.4$ and 10.4% at $pH = 2$ when combined with the best reported triple-junction solar cell. Additionally, by incorporating pores, we could relax the requirements for operation at extreme pH electrolytes which corrode the photoelectrodes limiting their lifetimes. On the other hand, we experimentally investigated the impact on the device operation when incorporating pores on single-junction and multi-junction thin-film silicon solar cells. A square array of circular pores with a period of $100\ \mu\text{m}$ and a diameter of $20\ \mu\text{m}$ was fabricated by optical lithography and dry etching. Upon introduction of the micro-scaled pores to various silicon based solar cells, we experimentally demonstrated that there is only minimal decrease in V_{oc} (less than 10% relative difference). For our triple junction (μ c-Si bottom /a-Si middle/a-Si top) solar cell, we maintained a high V_{oc} of 1983 mV after incorporating the pores. Given the fact that the impact is less pronounced on the a-Si compared to the μ c-Si cells, an all a-Si based triple junction solar cell deposited on a porous conductive carrier substrate (highly doped thin foil) could enable high solar to hydrogen efficiencies which can be maintained when upscaling. Such a device design offers a paradigm shift towards efficient, long-lasting and scalable devices.

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