

# Integrated photonic delay-lasers for reservoir computing

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## ABSTRACT

Currently, multiple photonic reservoir computing systems show great promise for providing a practical yet powerful hardware substrate for neuromorphic computing. Among those, delay-based systems offer a simple technological route to implement photonic neuromorphic computation. Its operation boils down to a time-multiplexing with the delay length limiting the processing speed. As most optical setups end up to be bulky employing long fiber loops or free-space optics, the processing speeds are ranging from kSa/s to tens of MSa/s. Therefore, we focus on external cavities which are far shorter than what has been realized before in such experiments. We present experimental results of reservoir computing based on a semiconductor laser, operating in a single mode regime around 1550nm, with a 10.8cm delay line. Both are integrated on an active/passive InP photonic chip built on the Jeppix platform. Using 23 virtual nodes spaced 50 ps apart in the integrated delay section, we increase the processing speed to 0.87GSa/s. The computational performance is benchmarked on a forecasting task applied to chaotic time samples. Competitive performance is observed for injection currents above threshold, with higher pumps having lower prediction errors. The feedback strength can be controlled by electrically pumping integrated amplifiers within the delay section. Nevertheless, we find good performance even when these amplifiers are unpumped. To proof the relevance and necessity of the external cavity on the computational capacity, we have analysed linear and nonlinear memory tasks. We also propose several post-processing methods, which increase the performance without a penalty to speed.

**Keywords:** neuromorphic computing, reservoir computing, delayed feedback, semiconductor laser, photonic integration

## 1. INTRODUCTION

Artificial neural networks (ANN) have played a significant role in the current AI boom, especially with the invention of ImageNet<sup>1</sup> as catalyst. ANNs may be efficient and versatile during operation, nevertheless they require complex and time-consuming algorithms to train the connection weights in the large network that forms the ANN. When interested in processing tasks and data where the temporal evolution is key, standard feed-forward ANNs are not sufficient and one needs to turn to recurrent neural networks (RNNs). The training of RNNs is a nonlinear problem due to feedback loops in the network and is far more involved than the training algorithms of feed-forward networks. Reservoir computing (RC) is paradigm that solves the training issue of RNNs in an efficient way.

RC offers a framework to exploit the transient dynamics within a RNN for performing useful computation. It has been demonstrated to have state-of-the-art performance for a range of tasks that are notoriously hard to solve by algorithmic approaches, e.g., speech and pattern recognition and nonlinear control. RC simplifies the training procedure for RNNs considerably. Its training procedure only acts on the output layer which consists of a linear combination of network states to generate the desired output signals. The connections of the RNN itself, which is now referred to as reservoir, remain fixed. During training, only the connections from the network

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to the output layer are adjusted. Due to this simplification, RC is very suited as framework for neuromorphic computing activities in photonics. Today, multiple photonic RC systems exist that can provide a practical yet powerful hardware substrate for neuromorphic computing.<sup>2</sup> Some examples include a network of semiconductor optical amplifiers,<sup>3,4</sup> an integrated passive silicon circuit forming a very complex and random interferometer, with nonlinearity introduced in the readout stage<sup>5</sup> and a semiconductor laser network based on diffractive coupling.<sup>6,7</sup> Applications can lie within telecom,<sup>8</sup> but also classification of ECGs<sup>9,10</sup> is a possible task among others. All these implementations have one thing in common: they rely on a network of photonic nodes that are spatially distributed and can be measured simultaneously.

However, the reservoir is not required to be a networked structure. In fact, any dynamical system with a high dimensional state space can be considered as reservoir substrate. We consider here specifically a semiconductor laser with delayed feedback as reservoir substrate. The concept of delay-based RC, using only a single nonlinear node with delayed feedback, was introduced some years ago by Appeltant *et al.*<sup>11</sup> Delay-based RC offers a simple technological route to implement photonic neuromorphic computation. Its operation boils down to a time-multiplexing with the delay arising from propagation in the external feedback loop, limiting the resulting processing speed. The system is easily scaled by tuning the delay length and only has one single physical node reducing the hardware complexity in photonic systems. The first working prototype was developed in electronics in 2011 by Appeltant *et al.*<sup>11</sup> and studied many configurations.<sup>12,13</sup> Several performant optical systems followed quickly after that.<sup>14-16</sup> Brunner *et al.*<sup>17</sup> employed off-the-shelf telecom equipment to experiment with a single-mode semiconductor laser subjected to optical feedback. The delay time in his experiments was around 80ns, which translates to a few meters of fiber. As most optical setups end up to be bulky employing long fiber loops or free-space optics, the processing speeds are limited in the range of kSa/s to tens of MSa/s.<sup>15,17</sup> To increase the processing speed of delay-based reservoir computing using a semiconductor laser with delayed optical feedback, one can integrate the laser and the delay both on the same photonic chip. In this way, by using a waveguide structure with a compact footprint, an external cavity structure can be implemented which is small enough to reach high processing speeds, yet still long enough to have sufficient dimensionality for good computing performance. In the long term, this integrated approach will lead to a robust and low-cost design. Recently, Takano *et al.*<sup>18</sup> have presented a photonic integrated circuit consisting of a distributed-feedback semiconductor laser, a semiconductor optical amplifier (SOA), a phase modulator, a short passive waveguide, and an external mirror for optical feedback. The external cavity length in this system reached 10.6mm, corresponding to a round-trip delay time of 254ps. However, only six virtual nodes could be stored within the delay line with node-spacings of 40 ps, not enough for good computational performance. This necessitated the authors to use masks with duration of multiple delay times, which slows down the computation speed. Several other types of semiconductor laser have been considered such as semiconductor ring lasers using the two available directional modes<sup>19-26</sup> and vertical-cavity surface-emitting employing the two polarization modes<sup>27-30</sup> and multi-mode lasers employing longitudinal modes.<sup>31</sup> In this paper, we will only focus on single-mode Fabry-Pérot type quantum-well semiconductor lasers.

The information processing performance of a semiconductor laser-based RC system is related to its dynamical behaviour both in the absence of external input and in the presence of said input. After the very first experiment by Brunner *et al.*,<sup>17</sup> other works have focused on understanding the fundamental properties of semiconductor laser-based RC for non-linear prediction tasks. In Ref. 32, it has been shown that the conditions to achieve good predictive performance are given by the injection locking, consistency, and memory properties of the system. More specifically, Bueno *et al.* found that the lowest prediction error for a non-linear prediction task occurs at the injection locking boundary. Note that in this work the laser was operating below or close to the solitary lasing threshold. Consistency, the ability of a system to have a similar response for the similar input signals, is widely regarded as key for good reservoir computing performance.<sup>32,33</sup>

Our goal is to show that a delay-based reservoir computer can be built using an indium-phosphide PIC, that combines active and passive elements and is built on the JePPIX platform.<sup>34</sup> The PIC integrates a semiconductor laser with an external cavity of 5.4 cm, which corresponds to a round trip time of 1170 ps. This allows for 23 nodes and a processing speed of 0.87 GSa/s. The longer waveguide based external cavity will also have more loss associated to it. Therefore, we will address in this work the question if amplification in the external cavity is needed or not. Contrary to other works,<sup>17,18,32</sup> the semiconductor laser itself will be driven far above solitary

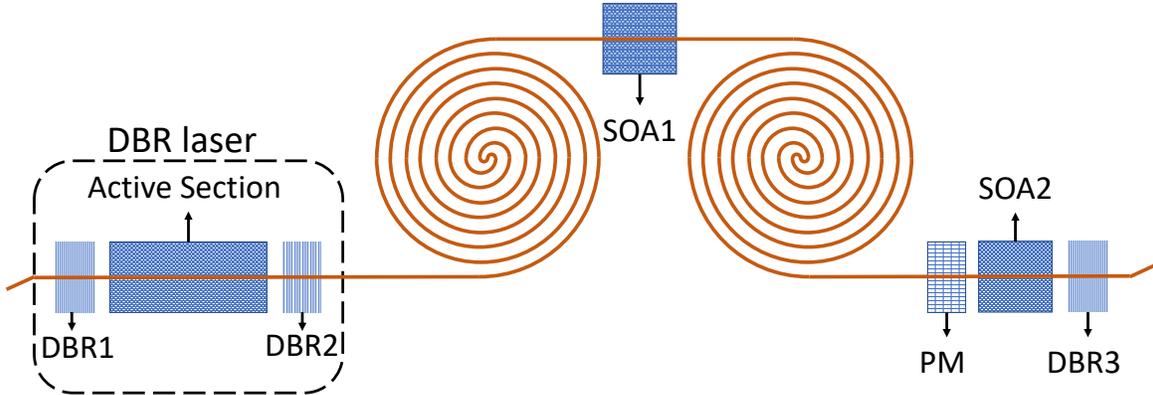


Figure 1. Schematic depiction of our InP-based photonic integrated circuit (PIC). The PIC consists of a laser structure followed by a delay line of 5.4 cm. DBR: Distributed Bragg Reflector, SOA: Semiconductor Optical Amplifier, PM: Phase Modulator.

lasing threshold to benefit from a better signal to noise ratio in the read-out, as well as faster internal dynamics. Finally, we will introduce post-processing schemes that do not penalize computational speed

## 2. EXPERIMENTAL SETUP

A schematic of our integrated device is shown in Fig. 1. It consists of a distributed Bragg reflector (DBR) laser structure and two spiral waveguides comprising the delay line. Two semiconductor optical amplifiers (SOA) are placed along the delay line to tune the feedback strength. A phase modulator is available to tune the feedback phase. We have not used this phase modulator due to setup complexity. Nevertheless, the phase is expected to play an important role.<sup>35</sup> At the end of the delay line a DBR element completes the feedback loop by reflection. This on-chip feedback loop has a round-trip time of  $\tau = 1170$  ps.

The device covers the whole 6mm width of the chip and has one optical input/output port on each side. The ports are angled with respect to the chip edge to minimize reflection. We employed lensed fibers to send optical signals in/out of these ports and a total of five electrical DC probes to operate the device. The first probe ( $I_{DBR1}$ ) was placed on the left DBR of the laser structure, in order to tune the spectral output of the laser. The second probe ( $I_L$ ) acted to supply the pump current to the laser. The following two probes ( $I_{SOA1}$ ,  $I_{SOA2}$ ) supplied current to the SOAs along the feedback line and the last probe ( $I_{DBR3}$ ) tuned the reflection spectrum of the DBR at the end of the feedback line. The active and SOA sections could be pumped up to a current of 40 mA, whereas the tuning currents of the DBRs could only be driven up to 10 mA.

The DBR laser has a threshold current of 15 mA. The free running lasing wavelength is centered at 1546.91 nm. In our setup, the on-chip laser can lock on the injection at the free running lasing wavelength or one of the side-modes, depending on the injected wavelength. It turned out that the RC performance is best when the injected field's wavelength is close to a side mode. Targeting the side mode allows for a higher injected power as the reflection of DBR1 is lower at the wavelength of the side-mode. Furthermore, DBR2 has a higher transmission for side modes than for the free running lasing wavelength. Injection locking on the side-mode is achieved at a wavelength of 1549.60 nm and the following DC probes configuration:  $I_{DBR1} = 8.28$  mA,  $I_{DBR3} = 1$  mA, and  $I_L = I_{SOA1} = I_{SOA2} = 40$  mA. The on-chip spectral parameters are not changed hereafter, meaning that the current supply to the two DBRs is not changed throughout the paper. A similar chip was recently used for random number generation.<sup>36</sup>

To test the RC performance of the laser integrated with a feedback loop, the setup shown in Fig. 2 is used. We use a wavelength tuneable CW laser to create the optical injection signal. The wavelength of this laser is set close to 1549.6 nm, but we still allow for a small detuning between the injection wavelength and the wavelength of the targeted side-mode of the laser. The CW light beam of the tunable laser is modulated using a 40GHz Mach-Zehnder modulator (iXblue MX-LN-40). This modulator is driven electrically by a 25GHz Arbitrary Waveform Generator (Keysight M8195A) set at a sample speed of 60 GSa/s.

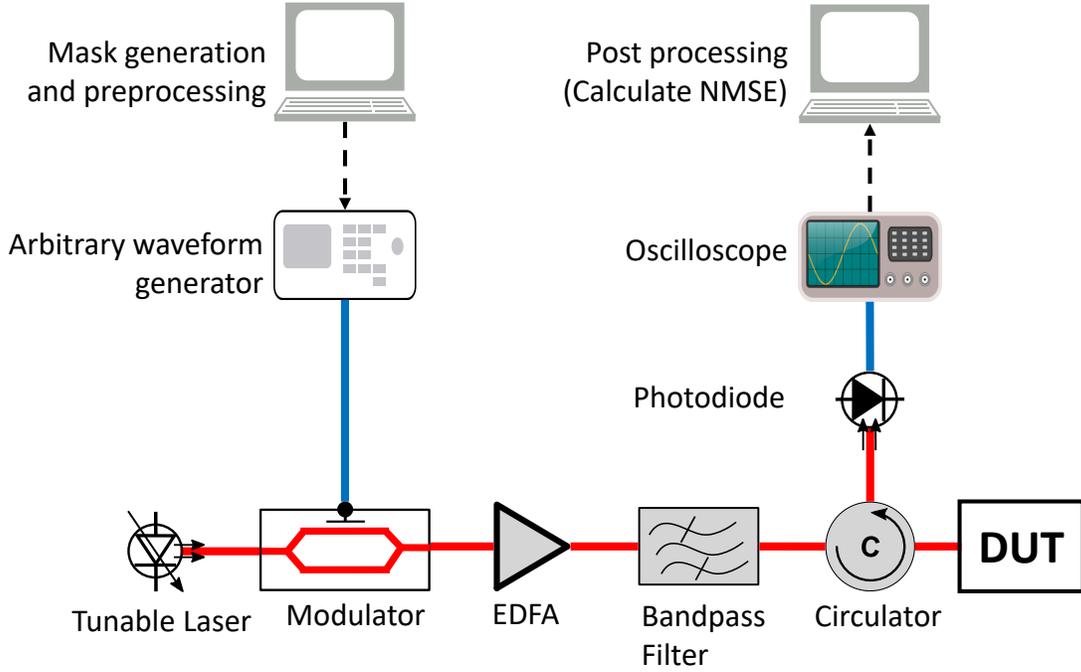


Figure 2. Schematic depiction of the setup used to measure the performance of our integrated delay-based reservoir computer. The device under test (DUT) is the PIC shown in Fig. 1

We employ the time-multiplexing scheme,<sup>37</sup> as introduced in 11, where the duration of one data sample matches the 1170 ps delay time. Note that there have been numerical and experimental studies, where the duration of a data sample does not match the delay time.<sup>15,38</sup> We, however, do not target this working regime. A similar time-multiplexing scheme can also be used for coherent ising machines.<sup>39</sup>

Any input data sample  $u_i$ , in our case originating from a discrete timeseries, is held constant for the duration of one delay time  $\tau$ . We then multiply this piecewise constant stream  $U(t)$  with a piecewise constant mask  $M(t)$  (that is periodic with a period of  $\tau$ ) to obtain the masked input stream  $J(t)$ . The piecewise constant levels of stream  $J(t)$  define the position of the virtual nodes equally spread over the delay line. It has been shown numerically<sup>40</sup> that the node separation, when using a semiconductor laser with delayed feedback, can be as short as a few tens of ps. As the sample rate of the AWG is set to 60 GSa/s, we use three AWG samples to define one mask node, leading to a mask node separation of  $\theta_M = 50$  ps such that 23 nodes fit within one round-trip in the delay loop. We thus generate a random mask with  $N_M = 23$  mask nodes with three possible values  $[0, 0.5, 1]$ . In our case the length of the mask is 20 ps shorter than the delay time, which is hard to match in practice. We believe this desynchronization will not adversely affect the performance of the RC scheme, since the mismatch is smaller than the node separation and we can accurately split the reservoir output in the readout layer.<sup>41</sup>

The modulated optical signal is next amplified in Fig. 2 using an Erbium doped fiber amplifier (Keopsys CEFA-C-BO-HP-B203). The broadband spontaneous emission noise, added to the optical signal by the amplifier, is removed by sending the light beam through an optical bandpass filter, that is centered around the injection signal's wavelength. The filtered signal is then fed into the laser using a circulator connected to a lensed fiber. The response of the laser is collected at the third port of the circulator and measured using an opto-electronic detector connected to a 63GHz real-time oscilloscope. The sampling rate of the oscilloscope was set to 40 GSa/s. This means that each mask node, with a duration of 50 ps, has 2 corresponding samples in the read-out signal.

### 3. RESULTS

We have studied the performance of a delay-based reservoir computer, which is designed on a photonic integrated chip. The integrated approach leads to a compact design as well as high computation speeds. We have studied the performance through the Santa-Fe timeseries benchmarking task.

With a conventional reservoir computing scheme, where the mask-imposed nodes coincide with the virtual nodes, we get a performance (best  $NMSE = 0.135$ ) which is slightly worse than those found in other works ( $NMSE$  around 0.1). However, we are working in different regimes. While previous works, such as,<sup>17,18,32,40</sup> operate in sub- or near threshold regimes, we operate our laser at pump currents well above the threshold current. We achieve a significant speed up compared to others,<sup>14,17</sup> who achieved speeds in the order of kSa/s and MSa/s respectively. The computation speed of our setup is 0.87 GSa/s, which is comparable to what Takano *et al.* achieved with additional pre- and post-processing steps.

We were able to improve the performance of the reservoir computer by using different post-processing routines. The first routine is using both readout samples within one mask-imposed node to form the output layer, unlike the conventional routine where we utilize one sample per mask-imposed node. The availability of extra states in the output layer, causes the reservoir computer to perform better. The extra states are not redundant in comparison with the rest, but rather enhance the state space. Since the mask-imposed node has a slightly longer duration than the timescale of the laser, we get two different state values from the transient response on the input. The best performance we achieved here is  $NMSE = 0.062$ .

The second post-processing routines takes the reservoir output for a duration of two delay times. This way we have a richer state space to perform the task and furthermore have access to a longer temporal memory inside this state space, since the last two input data points are present in the two delay times. This post-processing routine has consistently been the best performing out of the three and reaches an  $NMSE$  as low as 0.049.

We have seen that the best performance for Santa Fe timeseries prediction was found when we the injected signal's wavelength was close to a side-mode, with zero detuning between the injected wavelength and side-mode. We also observed that delay-based RC using semiconductor lasers can achieve very good performances at pump currents well above threshold, where most studies have focused on near-threshold operation. Lastly, we studied the memory capacity of our RC setup as the feedback in the setup is increased and we see a clear increase. Even when the SOAs in the delay line are turned off, we get a linear memory capacity around 8, which suggests that there is enough feedback already in the system without extra amplification. Possibly the performance can be improved further if this system were to be cascaded.<sup>42</sup>

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