

# Focused-Ion-Beam Fabrication of Slanted Grating Couplers in Silicon-on-Insulator Waveguides

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**Abstract**—We have designed and fabricated an efficient grating coupler for coupling light between optical fibers and silicon-on-insulator waveguides. The coupler consists of 88-nm-wide slits, etched at an angle of  $58^\circ$  to the surface normal. They are defined by direct etching with a focused ion beam, using iodine gas and an alumina hard mask. The measured efficiency is 46%.

**Index Terms**—Fiber coupler, focused-ion-beam (FIB), slanted grating coupler.

## I. INTRODUCTION

THE silicon-on-insulator (SOI) platform is a promising candidate for future ultracompact photonic integrated circuits because of its compatibility with complementary metal–oxide–semiconductor technology [1]. The high index contrast in this material system allows for the fabrication of short waveguide bends and, therefore, circuits with a high degree of integration. Coupling between a high index contrast waveguide and an optical fiber is primordial, but difficult due to the large mismatch between the optical modes. In literature, one proposes out-of-plane grating couplers as a solution to this problem [2]. Due to the large index contrast, the gratings are very compact and broadband. Furthermore, the out-of-plane approach makes polished facets unnecessary and enables wafer-scale testing of integrated optical circuits.

Most grating couplers up to now were fabricated with standard techniques based on optical lithography [3], [4]. Another approach is the direct etching of grating couplers in predefined silicon waveguides. This enables *in situ* analysis of integrated circuits. A promising method for the direct etching approach is focused-ion-beam (FIB), a common tool for device analysis and modification in electronics.

Microfabrication with FIB consists of hitting a substrate locally with high energy ions; in most commercial systems, like the FEI Dualbeam 600 used in this work, these are gallium ions with energies around 30 keV. In crystalline substrates, this process induces lattice damage, makes the top layer amorphous, and implants ions deeper into the substrate [5], [6]. Therefore,

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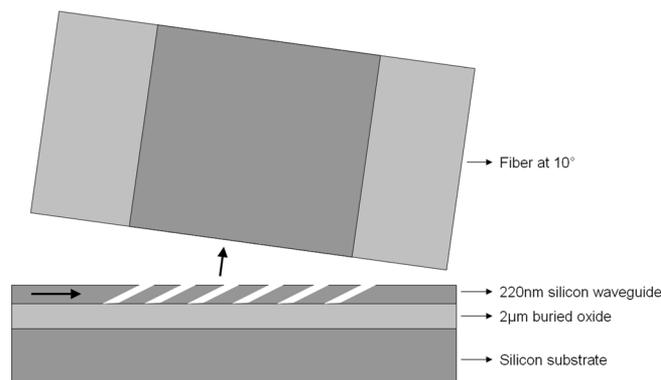


Fig. 1. Schematic drawing of the slanted fiber coupler.

direct etching of grating couplers with FIB has proven unsuccessful [7]. Nevertheless, there are fabrication strategies to minimize optical losses, such as high temperature annealing [8], or the use of a protective mask and a chemical etchant [9]. When carefully adopting these strategies, FIB is a tool that offers rapid prototyping flexibility, enables oblique etching, and has a sub-100-nm resolution. We will explore this unique combination for the fabrication of slanted grating couplers.

It was shown that shallow grating couplers can be etched with FIB [9]. However, these couplers require a two-step etching process and have a moderate coupling efficiency of 24%. An approach to boost the efficiency is the use of slanted slits that are etched entirely through the waveguide layer [10], as depicted in Fig. 1. In this letter, we present the design and fabrication of a slanted grating coupler, combining the flexibility of FIB with the higher efficiency of slanted grating couplers. In Section II, we discuss the design of a slanted fiber coupler, in Section III, we report on the fabrication with FIB, and in Section IV, we present the measurement results.

## II. SIMULATION

Regular shallow gratings in SOI are limited in efficiency due to diffraction of the first order into the substrate. This limit can be circumvented by a bottom distributed Bragg reflector mirror [2], or by a bottom gold mirror [3]. However, these greatly complicate the fabrication of integrated circuits in SOI. Another method to enhance the coupling to the upward first order is the use of slanted facets, in analogy to a blazed grating. Because it is difficult to control the depth of the slanted slits with FIB, we have chosen to design a slanted grating with slits through the entire top silicon layer. We use SOI wafers with 220-nm top silicon and 2- $\mu\text{m}$  buried oxide (Fig. 1). The gratings were simulated with the finite-difference time-domain (FDTD) method.

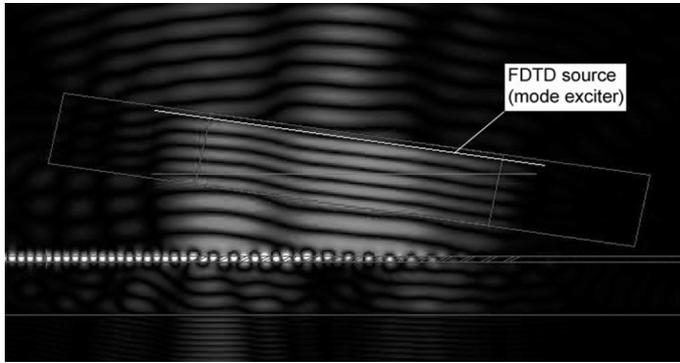


Fig. 2. FDTD simulation of the optimal grating with 64% efficiency. The plot shows the field pattern at a wavelength of 1550 nm. The mode exciter is located in the fiber, the mode sensor in the SOI waveguide.

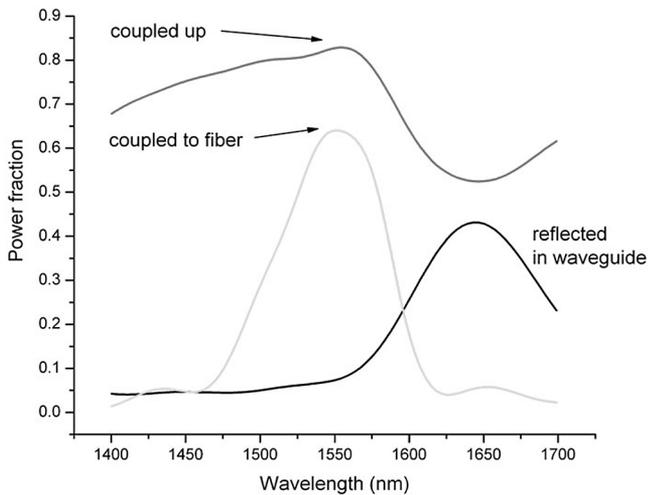


Fig. 3. Result of the slanted coupler optimization with an FDTD simulation. The curves present the power fractions coupled upwards, coupled into the fiber mounted at  $10^\circ$ , and reflected back into the waveguide.

The optimization was done by first scanning the complete parameter space, followed by a local quasi-Newton optimization. Although one can in principle couple light upwards by total internal reflection on a single slanted facet, there will be little overlap with the mode in the fiber in that case. To ensure more overlap with the large fiber mode, one needs to couple light upwards in a distributed way. This is feasible with—FIB fabricated—sub-100-nm slanted slits that allow tunnelling.

The optimum grating for  $10^\circ$  coupling has 87.5-nm-wide slits at an angle of  $58.4^\circ$  to the surface normal. The fiber is mounted at  $10^\circ$  to ensure a low second-order reflection in the waveguide. Our simulations were performed on a  $25\ \mu\text{m} \times 14\ \mu\text{m}$  base, and converged with a 10-nm mesh. A plot of the calculated field pattern at a wavelength of 1550 nm is shown in Fig. 2. The slanted grating coupler has a fiber-to-chip coupling efficiency of 64%, a period of 675 nm, and a 3-dB bandwidth of 100 nm. Fig. 3 shows the calculated power fractions that are diffracted upwards, coupled into the fiber, and reflected in the waveguide. For 1550-nm light the back reflection is only 7%. The total amount of power diffracted out of plane is 83% at 1550 nm. Although only part

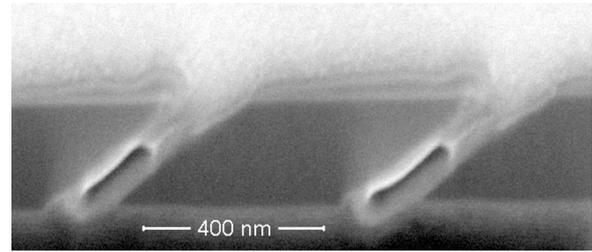


Fig. 4. Cross section of two slits of the slanted grating coupler. There is a good agreement with the FDTD designed grating.

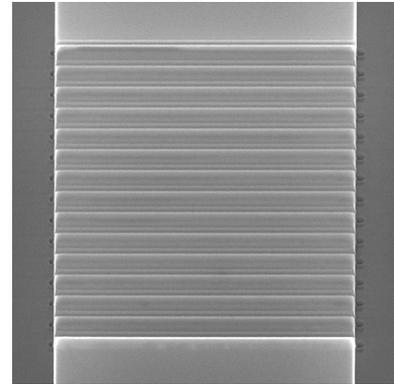


Fig. 5. Top view of the slanted grating coupler.

of this upward power is coupled into the fiber due to mode mismatch for a periodic grating, the mode mismatch can be decreased by varying slit width. However, due to fabrication complexity of varying slit widths, we have chosen only to design and fabricate periodic slanted gratings.

### III. FABRICATION WITH FIB

In previous work, we have demonstrated that low-loss fiber couplers can be fabricated with FIB [9]. By using  $\text{Al}_2\text{O}_3$  as hard mask and  $\text{I}_2$  as selective etchant, the loss by crystal damage can be minimized.  $\text{Al}_2\text{O}_3$  has a very low penetration depth for incident gallium ions and has a very low etch rate under iodine atmosphere.

Therefore, 50 nm of  $\text{Al}_2\text{O}_3$  was deposited on predefined SOI waveguides using electron beam evaporation. Simulations and experiments have shown that this thin layer has no influence on the propagation losses of light in the predefined waveguides. That is why we did not incorporate the layer in our simulations. To etch narrow slanted slits, we have mounted the sample under  $58^\circ$  relative to the ion beam, and scanned lines under an iodine atmosphere. Both hard mask and silicon are etched in the same run, where narrow slits are formed due to the large etch rate difference between alumina and silicon. The etch dose was optimized to etch down to the oxide buffer layer. We have noticed that the slit width depends strongly on the beam size. The beam current used was 50 pA, corresponding to a beam size of about 30 nm. A cross section of two grating slits is shown in Fig. 4, and a top view is shown in Fig. 5. For an etch dose of  $1.1 \times 10^{13}\ \text{Ga}^+/\text{cm}^2$ , the slits are etched down to the oxide buffer layer and the slit width corresponds to the simulations. The etch time for 15 slits of  $10\ \mu\text{m}$  wide is about 8 min. After

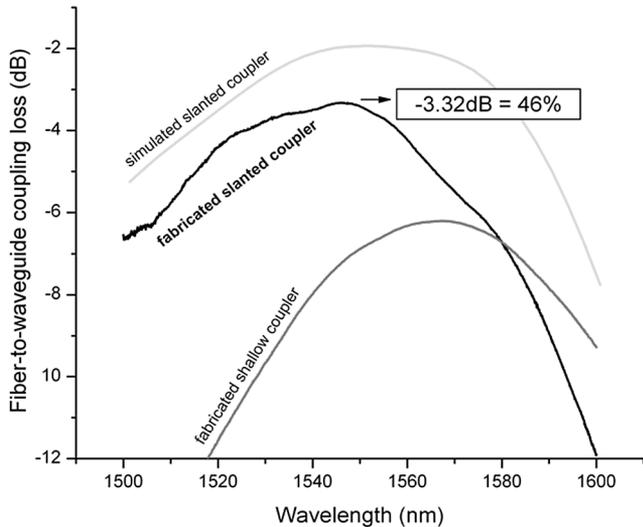


Fig. 6. Coupling spectra of simulated and fabricated slanted fiber coupler, as compared to a shallow grating coupler fabricated with optical lithography.

fabrication, the sample was baked for 2 h at 300 °C in a nitrogen atmosphere.

#### IV. MEASUREMENT AND DISCUSSION

To determine the coupling efficiency of the fabricated coupling structure, we have used a fiber-to-fiber transmission measurement for TE polarized light from a superluminescent light-emitting diode. The structure consisted of a regular shallow input coupler fabricated with optical lithography, a broad 10- $\mu\text{m}$  waveguide, and a slanted output coupler fabricated *in situ* with FIB. The coupling characteristic of the shallow couplers was measured in a separate setup with identical input and output couplers. The coupling efficiency of the slanted grating coupler is depicted in Fig. 6. It is calculated by subtracting—on a logarithmic scale—source spectrum and shallow coupler spectrum from the measured spectrum. We have extracted a maximum fiber-to-waveguide coupling efficiency of 46% and a 3-dB bandwidth of about 80 nm for the slanted fiber coupler fabricated with FIB.

To evaluate the discrepancy between simulated and measured coupling efficiency, we have investigated the fabrication tolerances of the slanted coupler. Period, slant angle, and slot width were varied within the measurement tolerance of the cross section in Fig. 4. The observed drop in efficiency was below 5%, therefore, bad grating parameters cannot fully explain the discrepancy. Instead, we consider the measured loss to be material

related. It is known that FIB etching generates optical losses in silicon [9]. Although the use of alumina as mask and iodine as enhancement gas considerably reduces these losses, we have noticed that an additional baking step at 300 °C is necessary to fabricate slanted couplers with efficiencies above 20%. We think that the baking step removes all remaining iodine and silicon iodide from the etched region. However, the damage caused to the silicon crystal remains unaltered after a temperature treatment at 300 °C. Therefore, we are convinced that silicon crystal damage in the region of the etched slits causes the discrepancy between theory and experiment.

#### V. CONCLUSION

We have reported on the design and FIB fabrication of a slanted grating coupler in an SOI waveguide. The coupler has a maximum measured coupling efficiency of 46% and can be etched *in situ*, anywhere on a wafer, in less than 10 min.

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